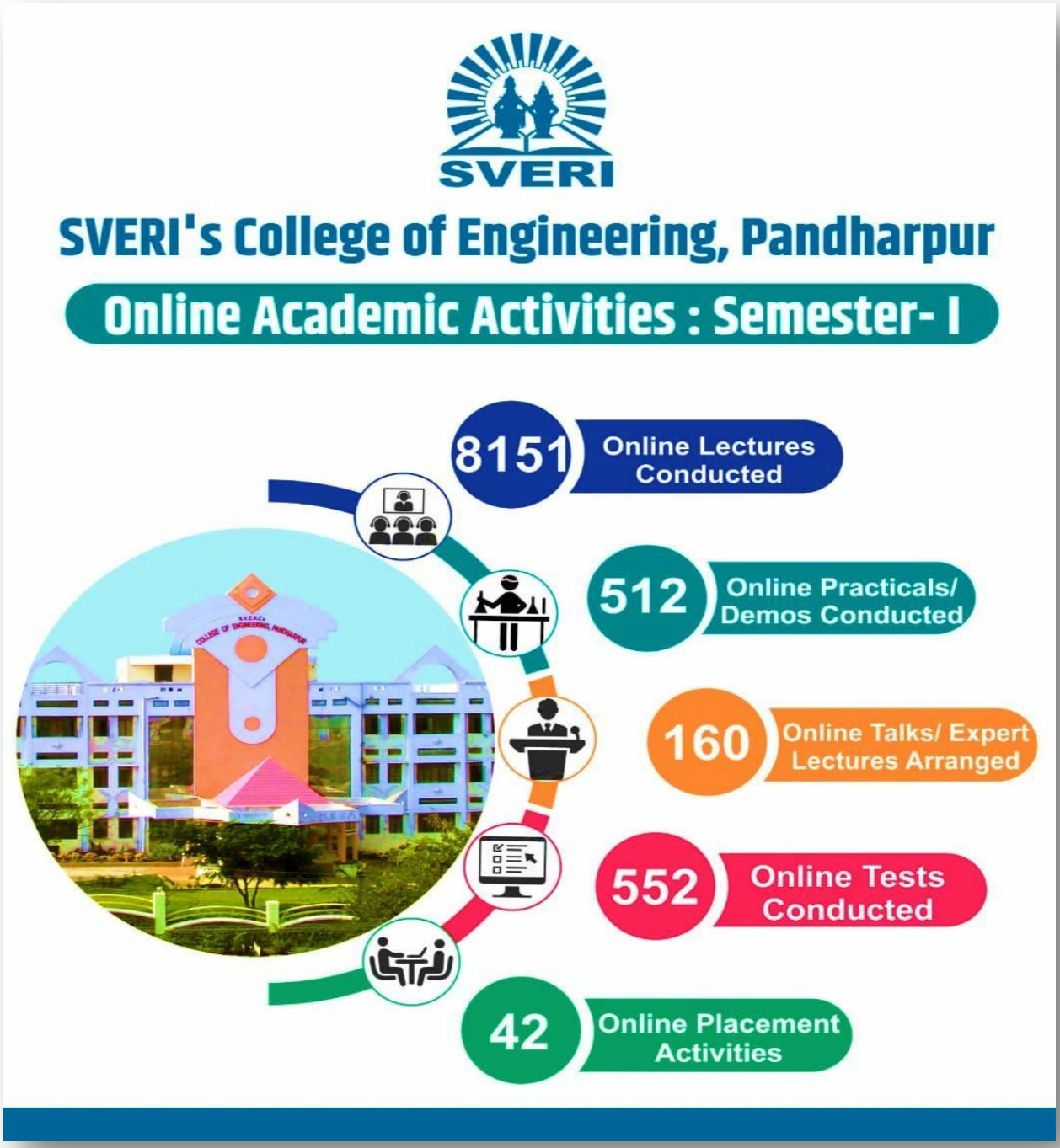
ICT TOOLS

In a recent pandemic situation, all the academic activities were conducted online. The summary of these activities is as below:



ADVANCED TECHNICAL TRAINING (ATT)

The purpose of the Advanced Technical Training (ATT) is to provide the knowledge of all technical subjects in Engineering & Science. Further, it gives clarity about the important questions asked in competitive exams like IES, SSC, JE, RTO, Indian Navy etc. Sample link of ATT activity is given below:

DEPARTMENT OF CIVIL ENGINEERING
https://att.sveri.ac.in/index.php
DEPARTMENT OF MECHANICAL ENGINEERING
https://att.sveri.ac.in/mech.php
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING
https://att.sveri.ac.in/ece.php
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
https://att.sveri.ac.in/cse.php
DEPARTMENT OF ELECTRICAL & ENGINEERING
https://att.sveri.ac.in/ee.php

VIDEO LECTURE LINKS

Class	Link
DEPARTMENT OF CIVIL ENGINEERING	
SY	https://docs.google.com/spreadsheets/d/e/2PACX-1vTD9H7mFkMsOBLzPPLD9vLMdZhRTWNgqBFI5cZ4zvnjsrkECUTIYrEzuHn1Zp9Tlyu_jeATUxGohXvD/pubhtml
TY-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vSocMr7MoWPWjwEfNGKP3a6qemX6t4C2P2gbWX2AGZ8CNlQErZn3BrYuDHRqu82ZcYQA4RW4T3XuKv7/pubhtml
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRqQyRKFNTBvx5kX-xyzrS2TrFD7L-QdWsXd8zQn4IlQoqwu7PQ-JWKILz2KGiLO9d3U2fGLkFUfe6X/pubhtml
BE	https://docs.google.com/spreadsheets/d/e/2PACX-1vTJrkCluB0qt1ajrrE7YGrgLda6lQiR3MDXw0NqoeOLEiLuouiBbivEgjeB9RJ9Qb7nr6QCWfUsB-4p/pubhtml
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING	
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRmBwgbIU1K4KXnJa4gDih32kBLfT1NJ5g0vmlGSeNl-2wr9UY635tayofN3asiPYFcZKjfaZnfBnt3/pubhtml
BE-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vT1Af7Mkt_4PZDX6OsaS1_h6wh-PBdXC4VNEvrBauQm2OJzKXakZmj1TfEYnMCrH16DmGSGQOJOEDGu/pubhtml
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING	
SY-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vSEEAyZbwTbSoct-zfDmpN8G0Kp7N0cYaziiGH86OHmkd0pWvmCzi9mzc4lKDr3kg/pubhtml
SY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRXkr5TIQtPSBFMlKRtc4zPQKVc9FkGHcotHUYstPfHlfCBCCNq4s2CeRCQtOjZgVlmam-845srOmtz/pubhtml
TY-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vQwBg2DSuqFW9rA7Bno94FQzpuJrg0X5sEKDFKJST6TDspleqXnXzwVBANIHJkrQA/pubhtml
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vSDbiSf8pB9Cu4r1QX_2PeRi-Nz_60y3SeRpfQ7iiEPsDoMVchpKART8-BLzA9Lyg/pubhtml
BE-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vTVpYN_Nothor2iFoYYVHMHXRW_QqEX-bNvKxZ_qoxsH12XaPSEqd0m4T0oUW2Svw/pubhtml
BE-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRCtVNhEcnzG8rBUzM_tky3N77Zf7y8gKmq1oPoS9EwLGMhK6hJURll9y3cRM16RQ/pubhtml
DEPARTMENT OF MECHANICAL ENGINEERING	
SY-B	https://docs.google.com/spreadsheets/u/1/d/e/2PACX-1vQXgVqIhTAgzPswW9dlDQETLVS4M26WLHZW5ipyAJ3Zy6n6OnjmT2o4D1ladLILWQfDrUWUyILRJhRE/pubhtml
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vQ0JvIpLR5QCnRmMI7CBoV1UYf7-feqrlVTXkeO1CC0X0YGpzSw7w0oZGfacpvvZjJcWsS5EVsRIZoA/pubhtml
BE-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vQcF3FCmDq0mpyWy8tCjlh76g1V-6hSddorxKMImoKiVq7TDHXc3uI4cugfn0sMRESX29zHvvJTRHn4/pubhtml
DEPARTMENT OF ELECTRICAL ENGINEERING	
SY	https://docs.google.com/spreadsheets/d/e/2PACX-1vRo4nVoq8S863x50wz1naosuI8rhZEcMy5GjQVCA5ypzOnbEjvpw-epWUOmM9dn1_Om_jsD9azar7rc/pubhtml
TY	https://docs.google.com/spreadsheets/d/e/2PACX-1vST9C_RlVjhYK_9nfEGHO2i6033-axs-McW2rvVPDF8vn-cNDiSAekGgqKhPFEs6owU1_RUZj3mAg0V/pubhtml
DEPARTMENT OF MASTER & BUSINESS ADMINISTRATION	
https://drive.google.com/file/d/1GcifbWt_b8z2yOuPLDhMbP9qnuUY5DFo/view?usp=sharing	

SMART BOARD

PURCHASE ORDER

Shri Vithal Education & Research Institute's

COLLEGE OF ENGINEERING, PANDHARPUR

P.B.No 54, Gopalpur - Ranjani Road, Gopalpur, Pandharpur - 413304, **District**: Solapur (Maharashtra)
Tel.: 7755990201 **Toll Free No.**: 1800-3000-4131 **e-mail.**: coe@sveri.ac.in **Website.**: www.sveri.ac.in
(Approved by A.I.C.T.E., New Delhi and Affiliated to Solapur University, Solapur)
Accreditated by The Indian Institution of Engineers (India), Kolkata and TCS, Pune.
NAAC Accreditated Institute, **NBA** Accredited All UG Programmes.
ISO 9001:2008 Certified Institute.

Ref.:- COEPR/2018-2019/1394 Date:- 27/10/2018

To,
Vaibhav Computers,
S.S.Front Road,Vijayapur,
Pin 586101.

Subject: - Purchase order for Computer Systems and Peripheral Devices.
Ref.:- Your Tender Quotation dated 06/09/2018 and further negotiation.

Dear Sir,
With the above reference, we are pleased to place the Purchase Order for Computer Systems and Peripheral Devices for our College as per the details given below.

Sr. No.	Particulars	Qty	Rate per unit (Rs.)	GST %	Amount in including GST(Rs.)
01.	**Make: HP** i5 8500 Processor Base Frequency 3.00 Hz, Max Turbo Frequency 4.10 Hz Cache 9 MB Smart Cache BUS Speed 8 GT/s DMI3 TDP, 65W, RAM- 4GB, HDD-1TB, Inbuilt Graphics Card, Monitor- 21.5".	06	45000.00	18	318600.00
02.	**Make: HP** i5 8500 Processor Base Frequency 3.00 Hz, Max Turbo Frequency 4.10 Hz Cache 9 MB Smart Cache BUS Speed 8 GT/s DMI3 TDP, 65W, RAM- 8GB, HDD-1TB, Inbuilt Graphics Card, Monitor- 21.5".	03	47000.00	18	166380.00
03.	**AcerVeritoni3 DOS** Intel H110 ,Ci3 7100 ,4GB DDR3 ,1 TB HDD ,No ODD ,USB K+M ,Free DOS,18.5" non TCO Monitor ,MT Chassis/ 200 W SMPS.	50	27500.00	18	1622500.00
04.	**HPZ240Tower Workstation** E3-1225v6 3.3 8M GT2 4C ,8GB DDR4-2400 nECC (1x8GB) RAM,1TB,NVIDIAQuadro P1000 4GB,Win 10 Pro 64 for Workstations INDIA,DVDRW, 21.5"HP monitor.	05	110000.00	18	649000.00
05.	Epson LQ 1310 Dot matrix printer 24 pin.	10	13200.00	18	155760.00
06.	HP laserjet 1020 plus Printer.	08	9800.00	18	92512.00
07.	Raptor IR IWB Smart Board RYJC085C Ceramic Based Smart Board.	03	36000.00	18	127440.00
08.	Computer Sound: I Ball 2.0 Speaker.	08	850.00	18	8024.00
09.	Transcend DDR3 RAM 2GB.	55	800.00	18	51920.00
10.	EPSON EB S41 3300 lumen,versatile SVGA Projector HDMI Inputs, Wifi connectivity Long life.	15	25300.00	28	485760.00
			Total		**3677896.00**
	In Words: Rs. Thirty Six Lacs Seventy Seven Thousand Eight Hundred Ninety Six Only.				

The order is subject to the following terms and conditions:

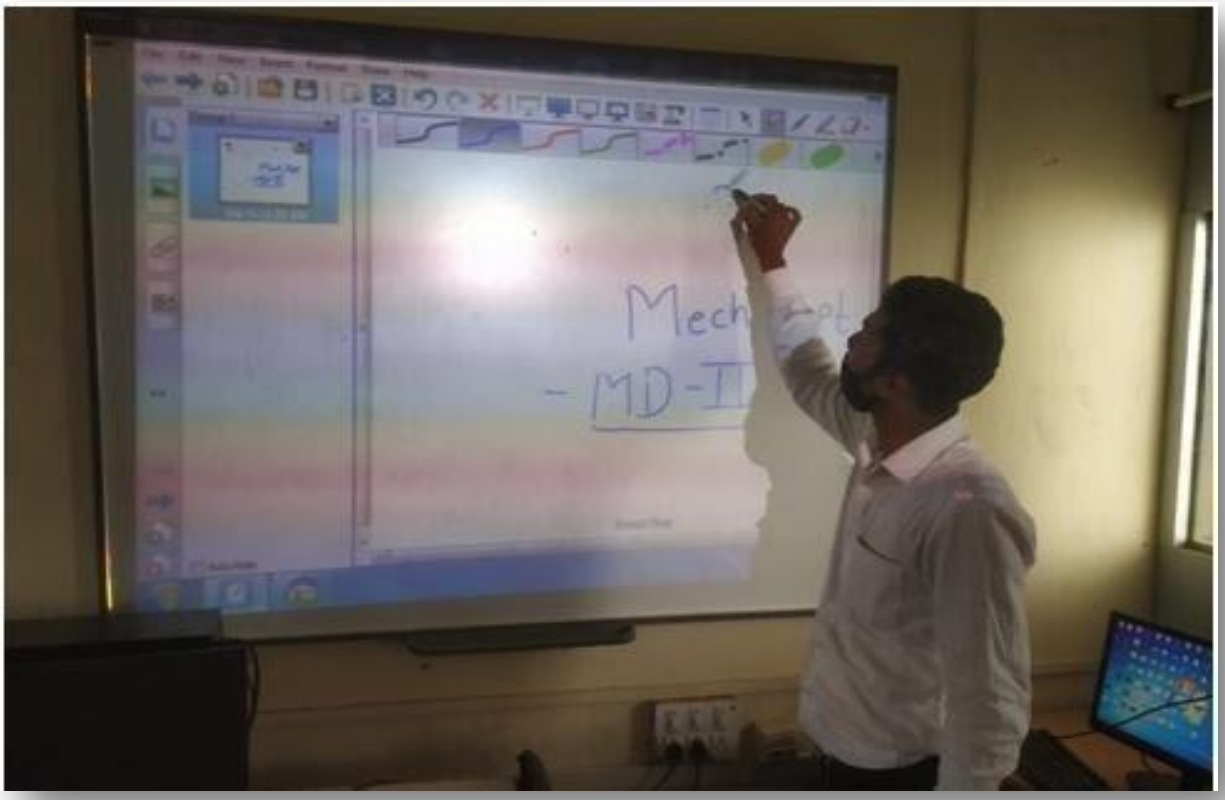
1. Acceptance of material is subject to prior inspection and approval from our side.
2. Prices are inclusive of all taxes,GST, duties, packing, forwarding, transportation up to our institute and installation..
3. Delivery within one week.
4. Warranty: a)Sr.No.01 to 04: Three years onsite comprehensive warranty from the date of satisfactory demonstration.
   b)Sr.No.05 to 09: One years onsite comprehensive warranty from the date of satisfactory demonstration.
   c) Sr.No.10: Two years onsite comprehensive warranty on projector and One year onsite comprehensive warranty on lamp from the date of satisfactory demonstration.
5. Payment: 100% against successful installation & satisfactory demonstration.
6. TDS, if any,will be deducted as per Government rules.

Yours faithfully,
B. Ronge
(Dr. B. P. Ronge)
PRINCIPAL

COLLEGE OF ENGINEERING PANDHARPUR

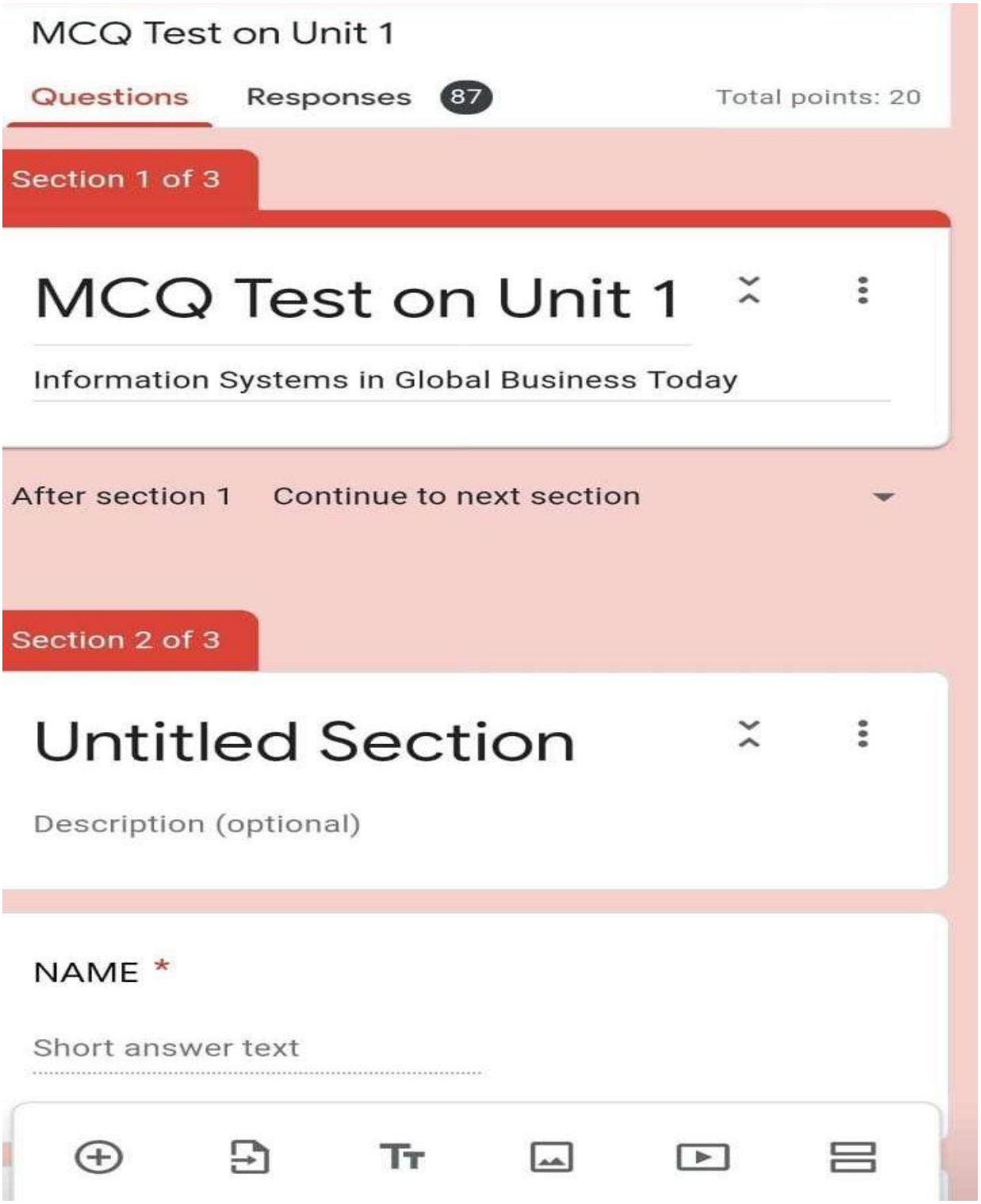
Received


LITERATURE REVIEW
172 3 173


Mech
- MD-I

GOOGLE FORMS

SAMPLE 1


MCQ Test on Unit 1
Questions
Responses 87
Total points: 20
Section 1 of 3
MCQ Test on Unit 1
Information Systems in Global Business Today
After section 1
Continue to next section
Section 2 of 3
Untitled Section
Description (optional)
NAME *
Short answer text

Section 3 of 3

Untitled Section

Description (optional)

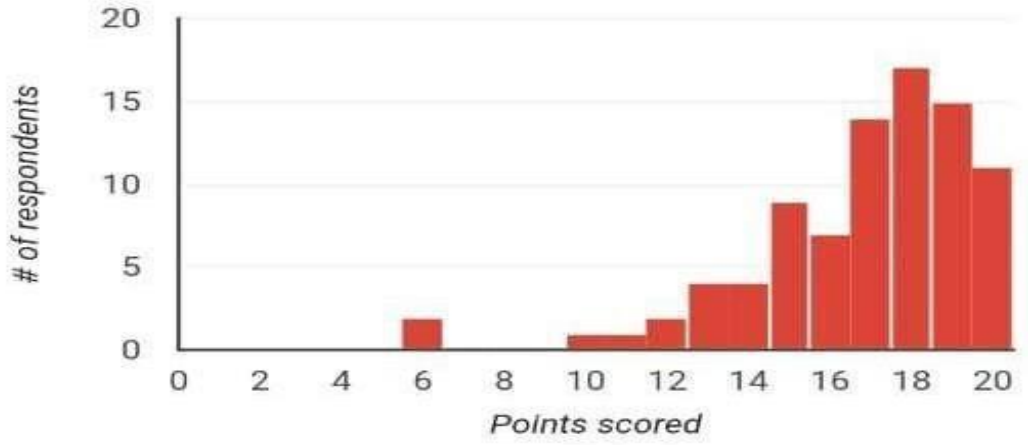
1. Main task of Management information systems (MIS) is *

- create and share documents that support day-today o...
- process business transactions (e.g., time cards, paym...
- capture and reproduce the knowledge of an expert pro...
- use the transaction data to produce information for m...

2. If a university sets up a web-based information system that faculty could access to record student grades and to advise students, that would be an example of a/an *

Average	Median	Range
16.79 / 20 points	17 / 20 points	6 - 20 points

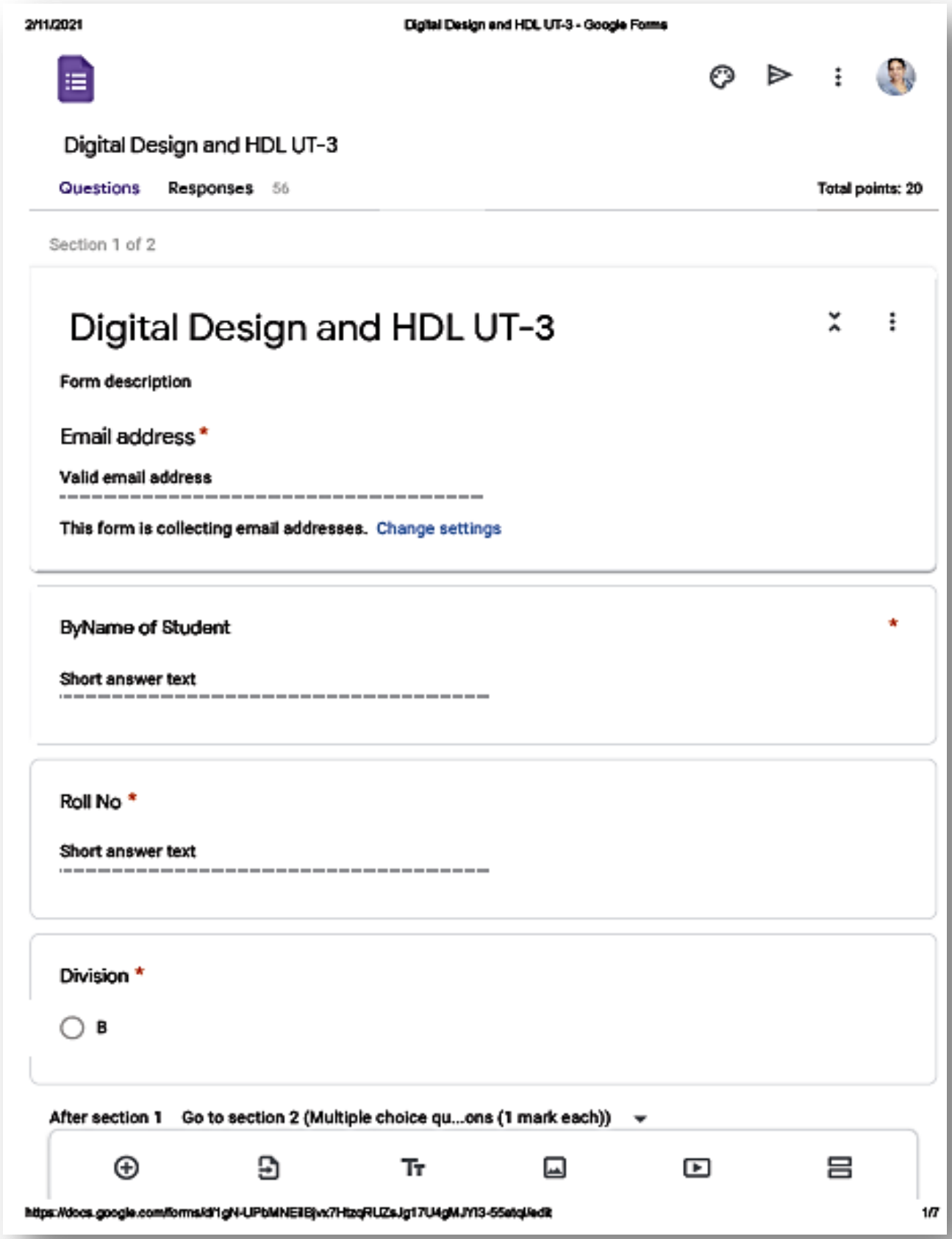
Total points distribution



Frequently missed questions

Question	Correct responses
7. Which type of information system does not actually support decision making?	42 / 87
13. The MIS helps the _____ management in goal setting, strategic planning and evolving the business plan and their implementations.	27 / 87

SAMPLE 2


2/11/2021
Digital Design and HDL UT-3 - Google Forms
Digital Design and HDL UT-3
Questions
Responses 56
Total points: 20
Section 1 of 2
Digital Design and HDL UT-3
Form description
Email address *
Valid email address
This form is collecting email addresses. Change settings
ByName of Student *
Short answer text
Roll No *
Short answer text
Division *
B
After section 1 Go to section 2 (Multiple choice qu...ons (1 mark each))
https://docs.google.com/forms/d/1gN-UPbMNEIBjvx7HtzqRUZsJg17U4gMJY13-55atqU/edit
1/7

Section 2 of 2

Multiple choice questions (1 mark each)

Description (optional)

1. Which hardware description language is more popular in Europe? *

- a) VHDL
- b) System log
- c) Verilog
- d) C

2. In verilog `h1234 is a

- A. 16 bit hexadecimal number
- B. 32 bit hexadecimal number
- C. 4 bit hexadecimal number
- D. It is invalid notation

3. Which logic level is not supported by Verilog?

- A. U
- B. X
- C. Z

4. If A= 4`b011 and B= 4b`0011, then the result of A**B will be

- A. 6
- B. 9
- C. 27
- D. Invalid expression

5. Verilog is an IEEE standard

- A. IEEE 1346
- B. IEEE 1364
- C. IEEE 1394
- D. IEEE 1349

6. Externally, a output port must always connected to a

- A. net only
- B. a reg only
- C. either net or reg
- D. None of the above

7. Initial value of a=1 and b=2, then what will be final value if ... always @ (posedge clock)

A. a= 2, b=1

B. a= 1, b=2

C. Both a and b will have same value either 0 or 1

D. None of the above

8.In a pure combinational circuit is it necessary to mention all the inputs in sensitivity list?

A. No

B. Yes

C. It depends on the coding style

D. None of these

9. Which is not a correct method of specifying time scale in verilog?

A. 1ns/1ps

B. 10ns/1ps

C. 100ns/100ps

D. 100ns/110ps

10. The semantics of an "&" operator depends on the number of operands.

A. True

B. False

11. An "if" statement must always be inside of an "always" block

- A. True
- B. False

12. Which of the following is true about the always block?

- A. There can be exactly one always block in a design.
- B. There can be exactly one always block in a module.
- C. Execution of an always block occurs exactly once per simulation run.
- D. An always block may be used to generate a periodic signal.

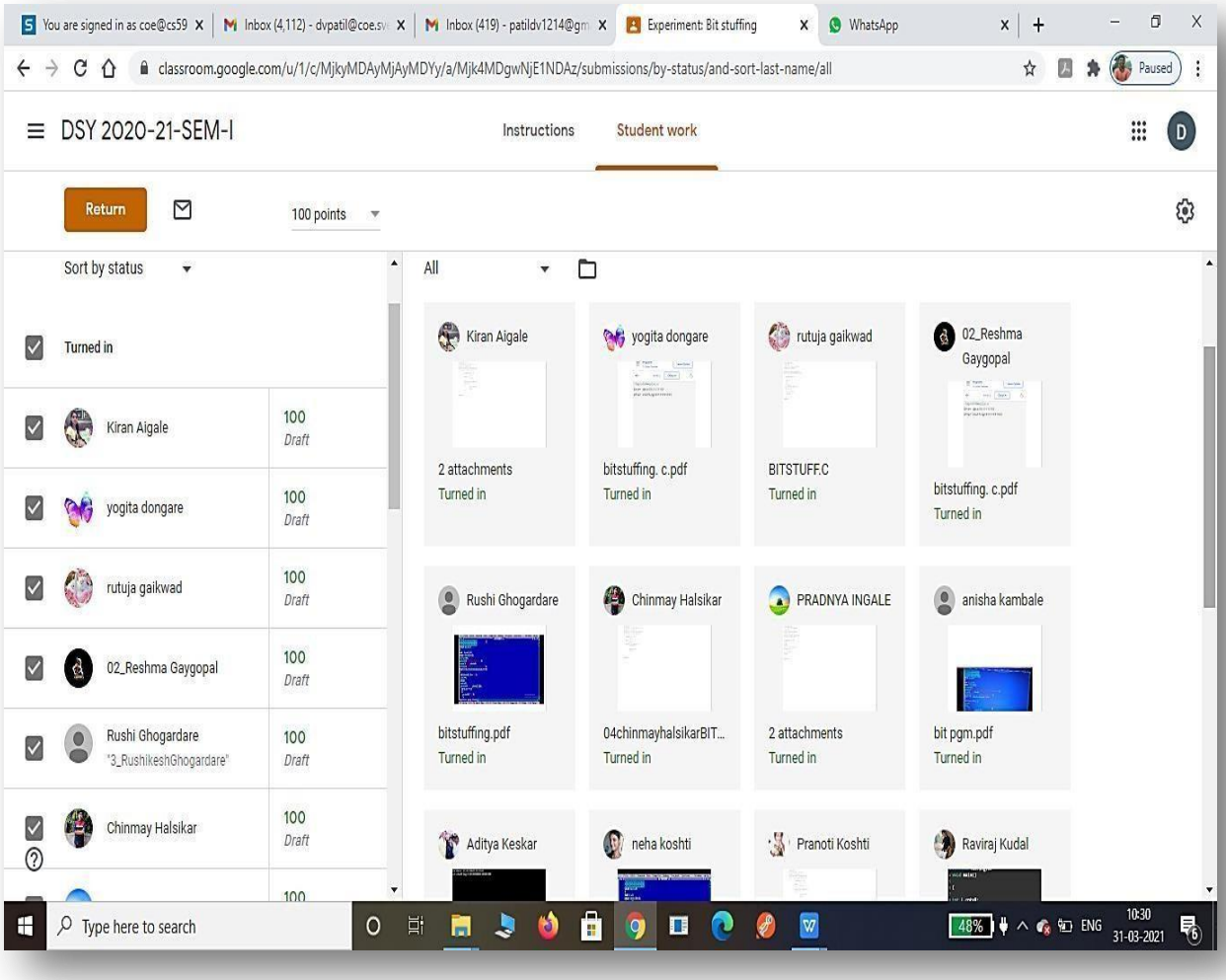
13. Consider the following choices below. To comment Verilog Code, one may use: I. A "Double-slash" // for a single-line comment. II. Multiple "Double-slashes" (one per line) for a multiple-line comment.III. A "Block-comment" /* */ for a single-line comment.IV. A "Block-comment" /* */ for a multiple-line comment.

- A. I and II
- B. I and IV
- C. II and III
- D. I, II, and IV
- E. I, II, III and IV

14. Verilog is case sensitive

GOOGLE CLASSROOM

SAMPLE 1


You are signed in as coe@cs59
Inbox (4,112) - dvpatil@coe.sv
Inbox (419) - patildv1214@gm
Experiment: Bit stuffing
WhatsApp
classroom.google.com/u/1/c/MjkyMDAyMjAyMDYy/a/Mjk4MDgwNjE1NDAz/submissions/by-status/and-sort-last-name/all
Paused
DSY 2020-21-SEM-I
Instructions
Student work
Return
100 points
Sort by status
All
Turned in
Kiran Aigale
100
Draft
yogita dongare
100
Draft
rutuja gaikwad
100
Draft
02_Reshma Gaygopal
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Draft
Rushi Ghogardare
"3_RushikeshGhogardare"
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Chinmay Halsikar
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02_Reshma Gaygopal
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Rushi Ghogardare
bitstuffing.pdf
Turned in
Chinmay Halsikar
04chinmayhalsikarBIT...
Turned in
PRADNYA INGALE
2 attachments
Turned in
anisha kambale
bit pgm.pdf
Turned in
Aditya Keskar
neha koshti
Pranoti Koshti
Raviraj Kudal
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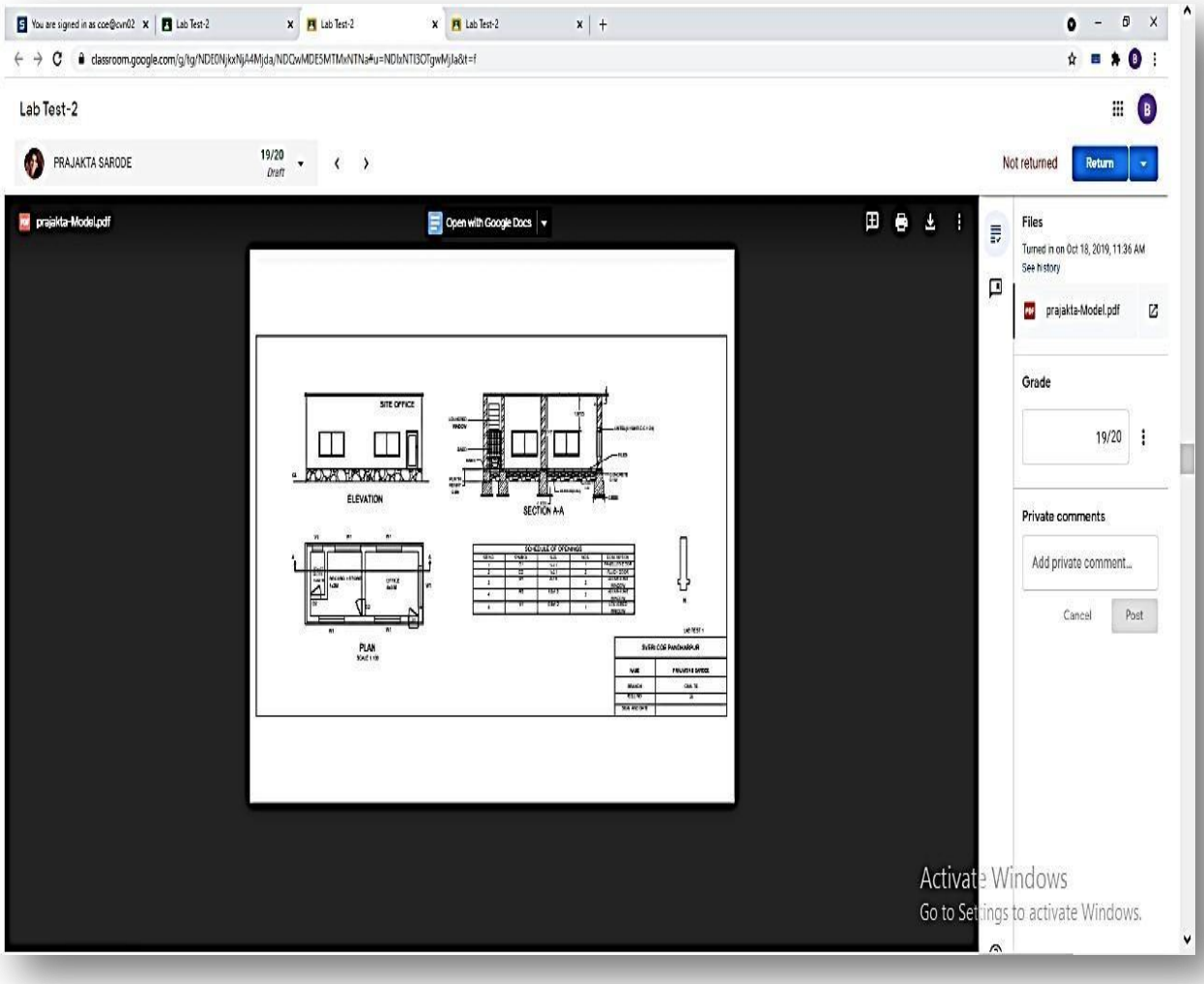
10:30
31-03-2021

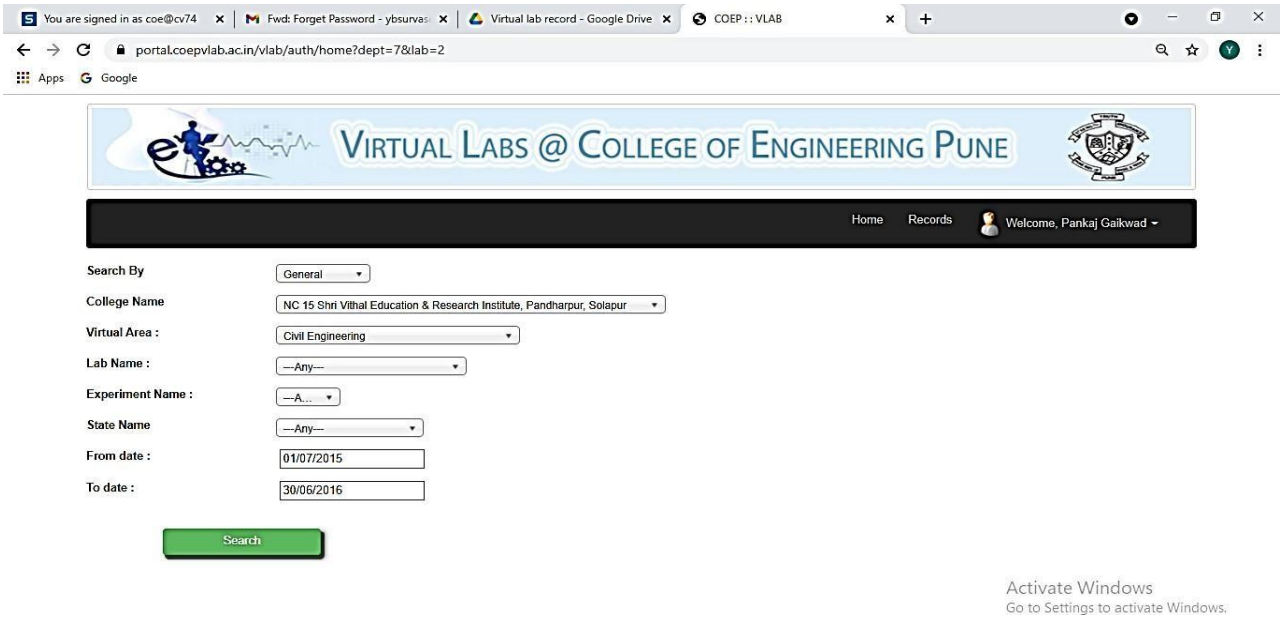
SAMPLE 2


You are signed in as cce@cvn02
Lab Test-2
classroom.google.com/g/tg/NDE0NjkxNjA4Mjda/NDCwMDE5MTMxNTNa#u=NDIxNTI3OTgwMjJa&t=f
Lab Test-2
PRAJAKTA SARODE
19/20
Draft
Not returned
Return
prajakta-Model.pdf
Open with Google Docs
SITE OFFICE
ELEVATION
SECTION A-A
PLAN
Files
Turned in on Oct 18, 2019, 11:36 AM
See history
prajakta-Model.pdf
Grade
19/20
Private comments
Add private comment...
Cancel
Post
Activate Windows
Go to Settings to activate Windows.

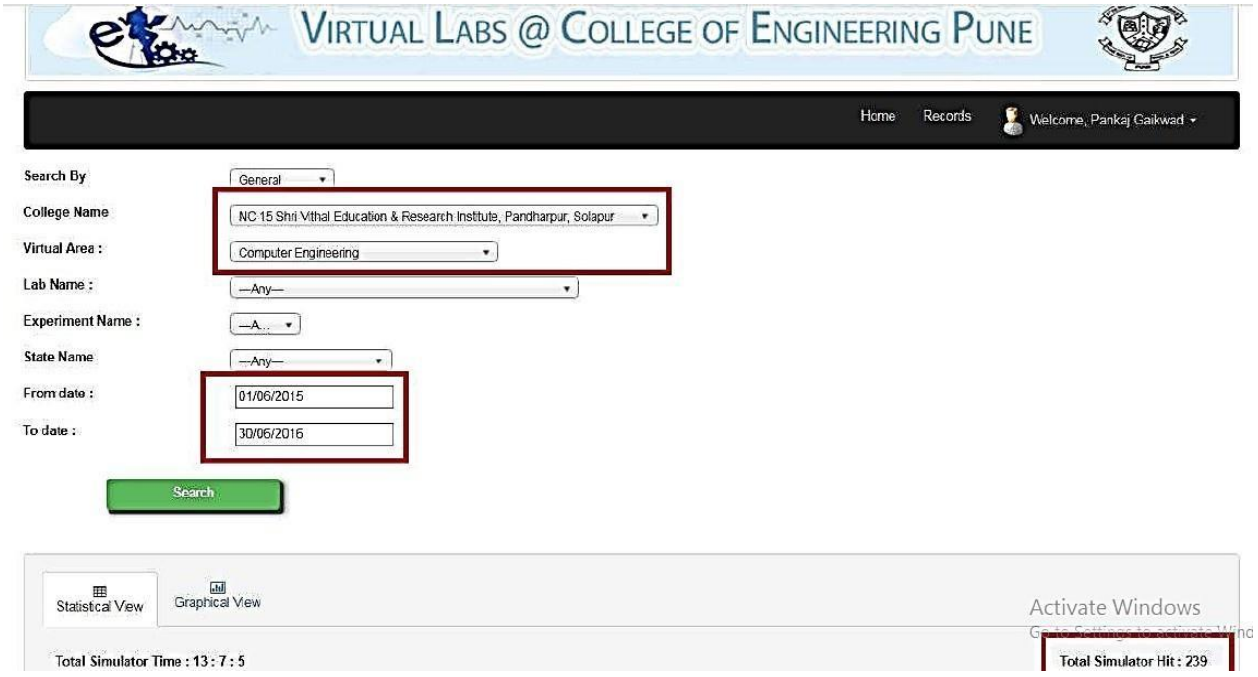
VIRTUAL LAB

2015-16

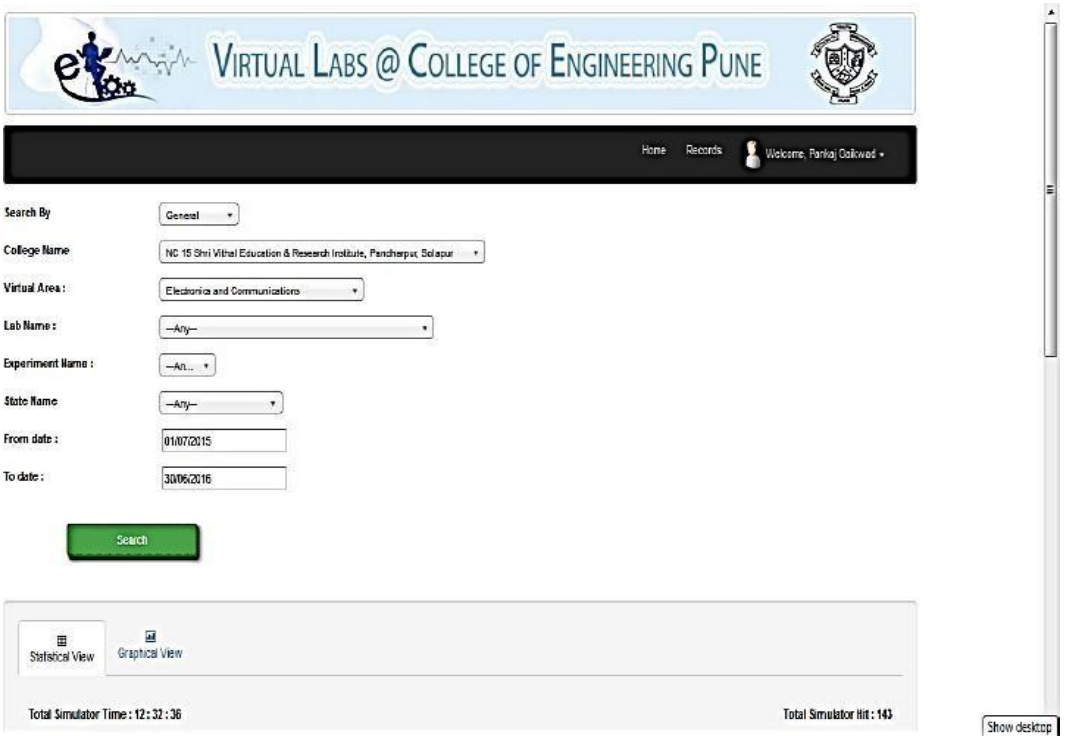
Civil Engineering



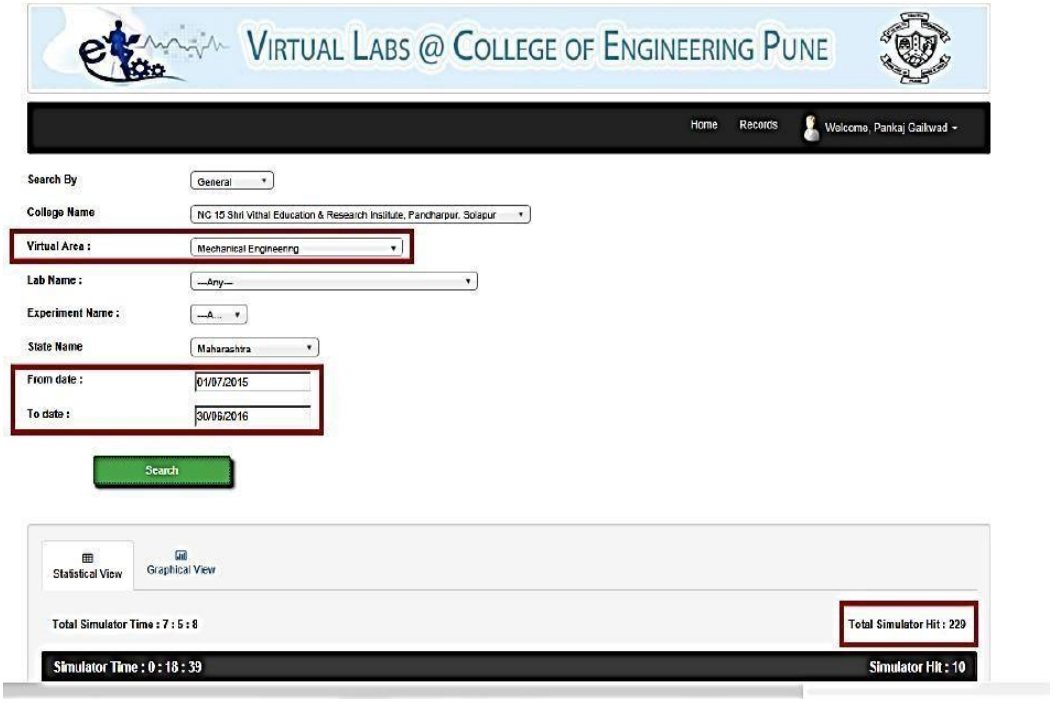
Computer Science & Engineering



Electronics & Telecommunication Engineering

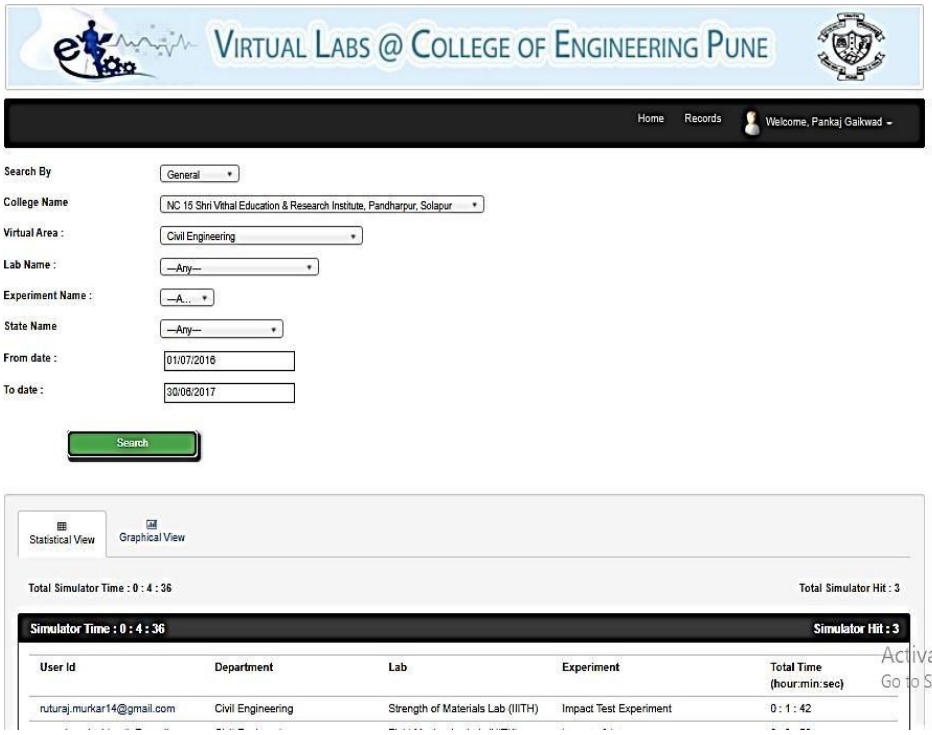


Mechanical Engineering

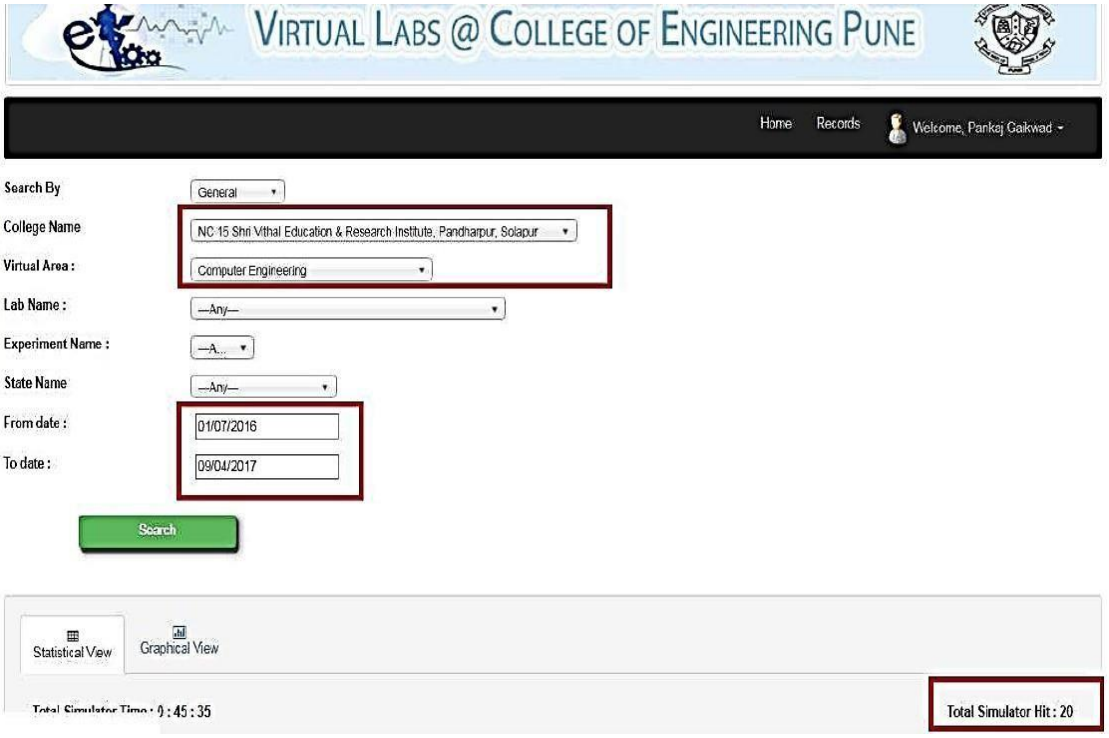


2016-17

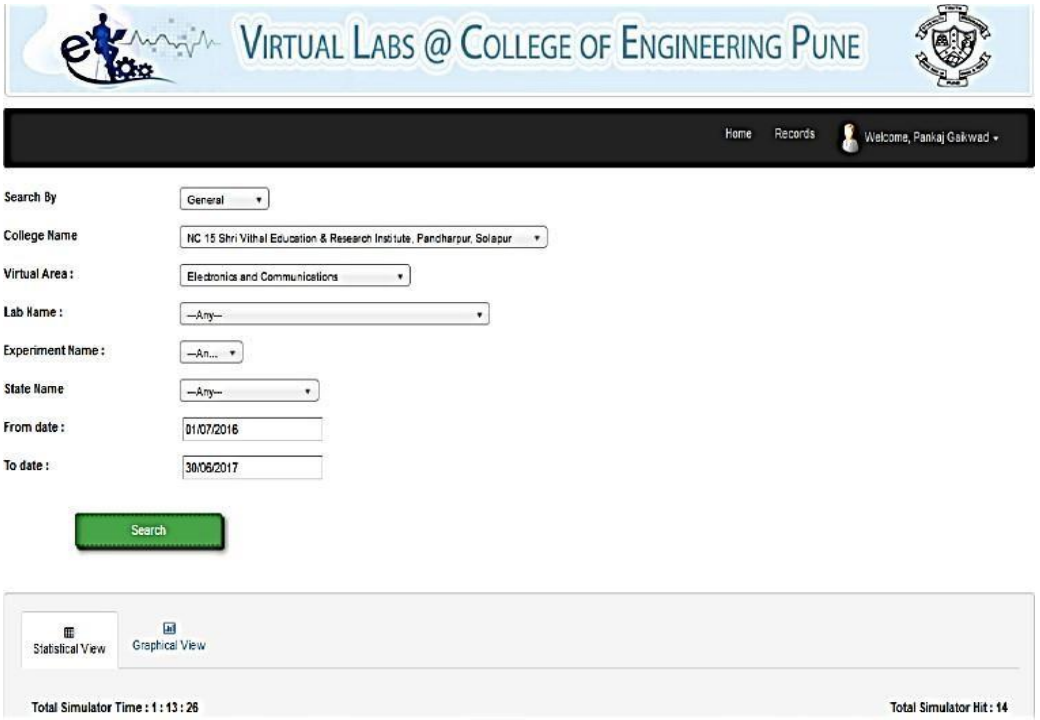
Civil Engineering



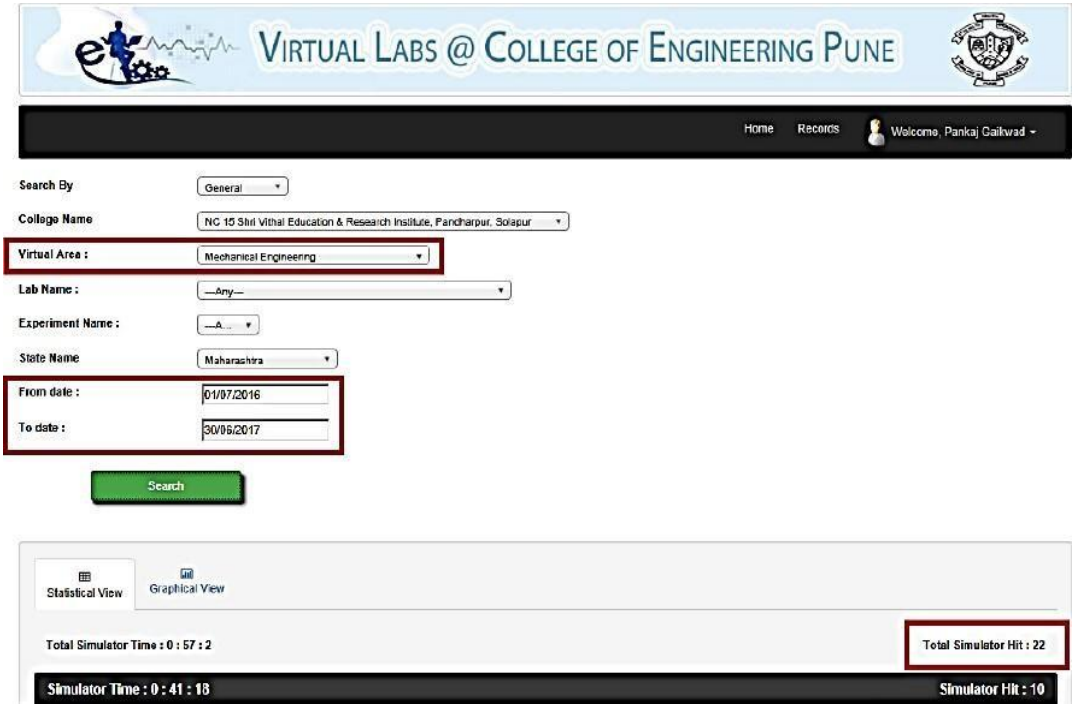
Computer Science & Engineering



Electronics & Telecommunication Engineering

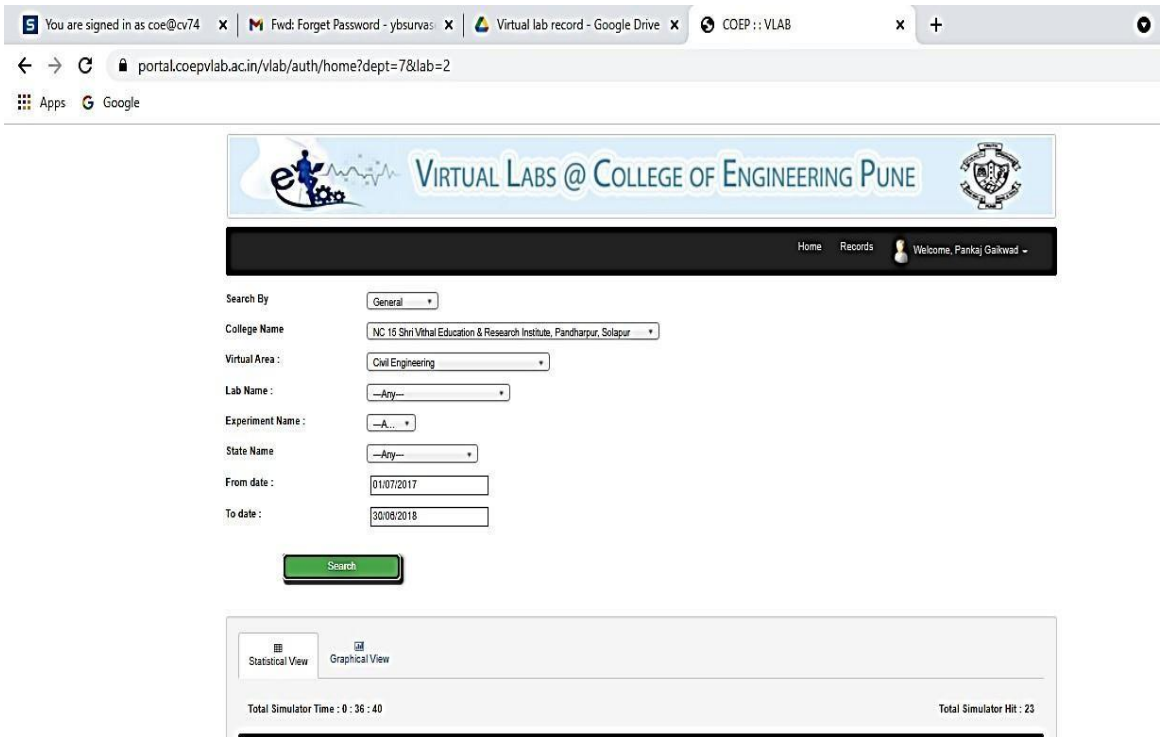


Mechanical Engineering

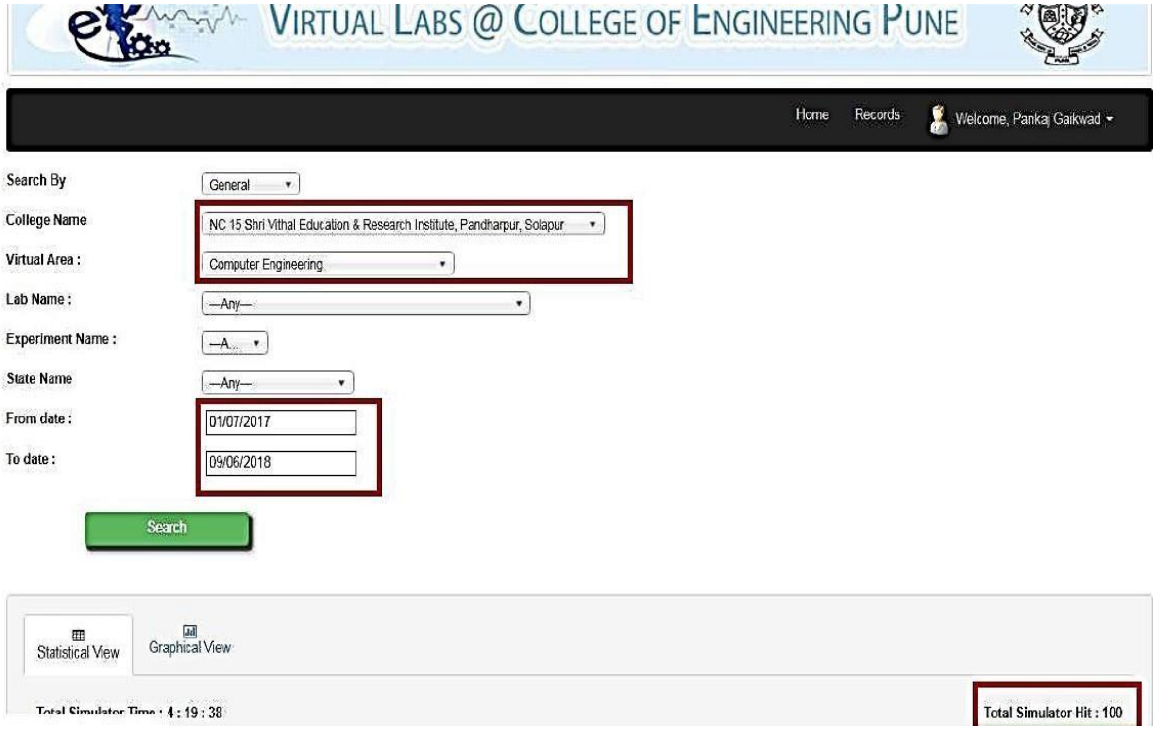


2017-18

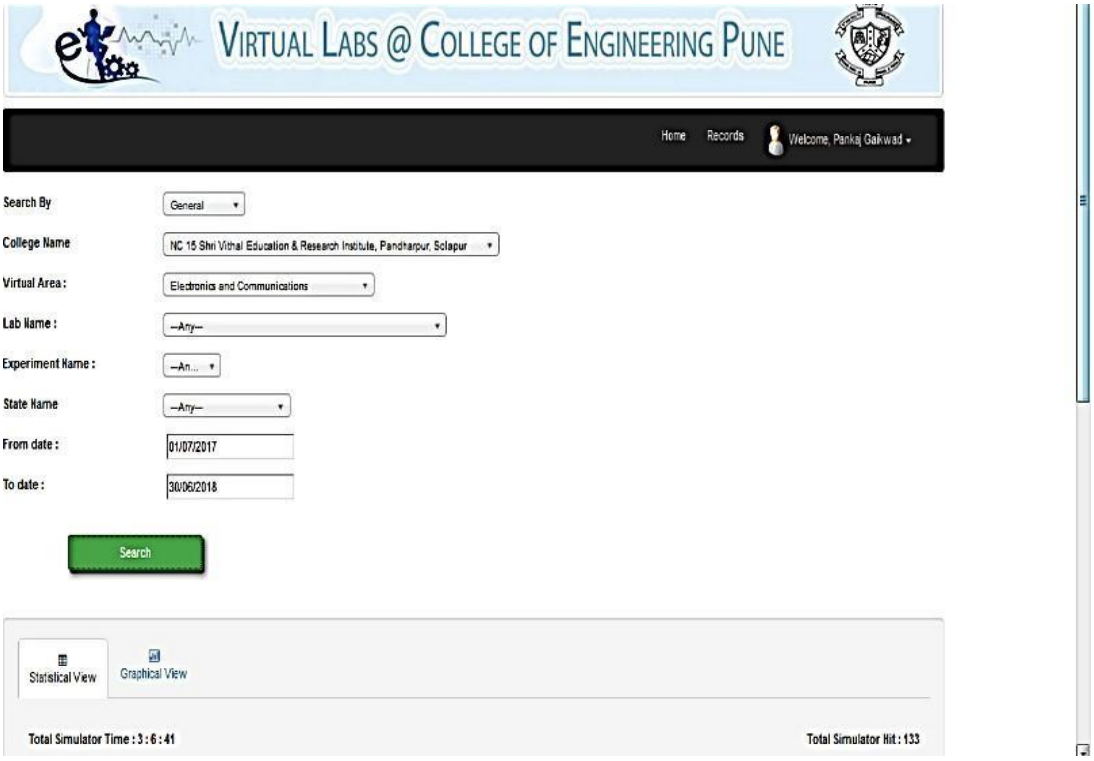
Civil Engineering



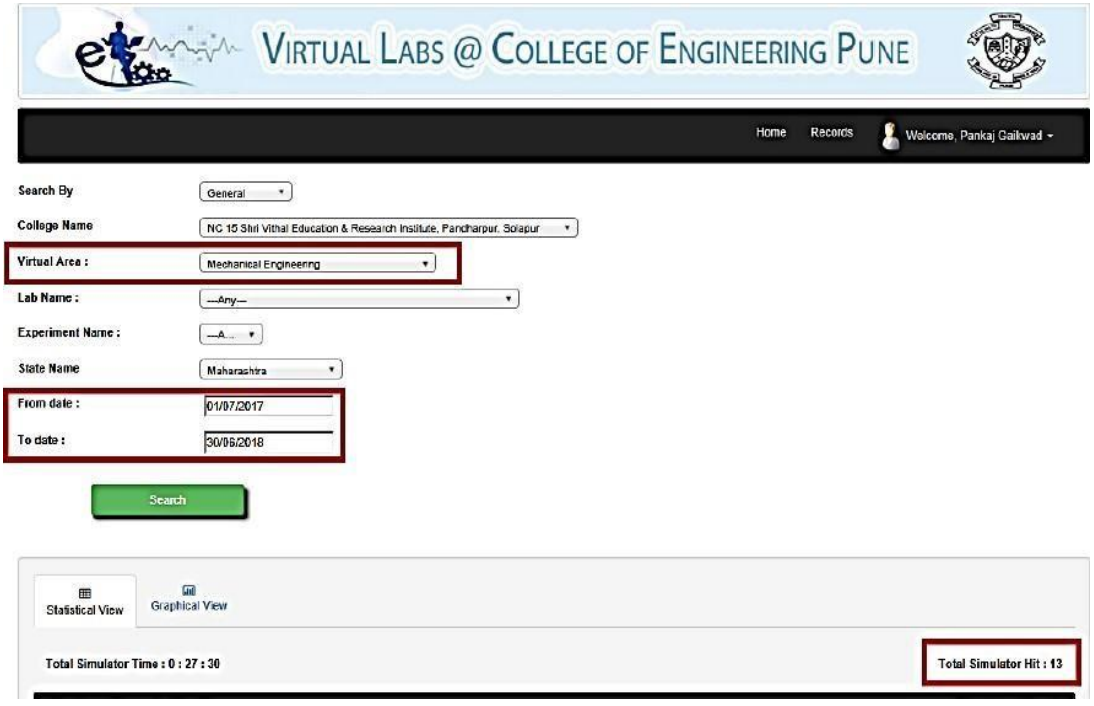
Computer Science & Engineering



Electronics & Telecommunication Engineering

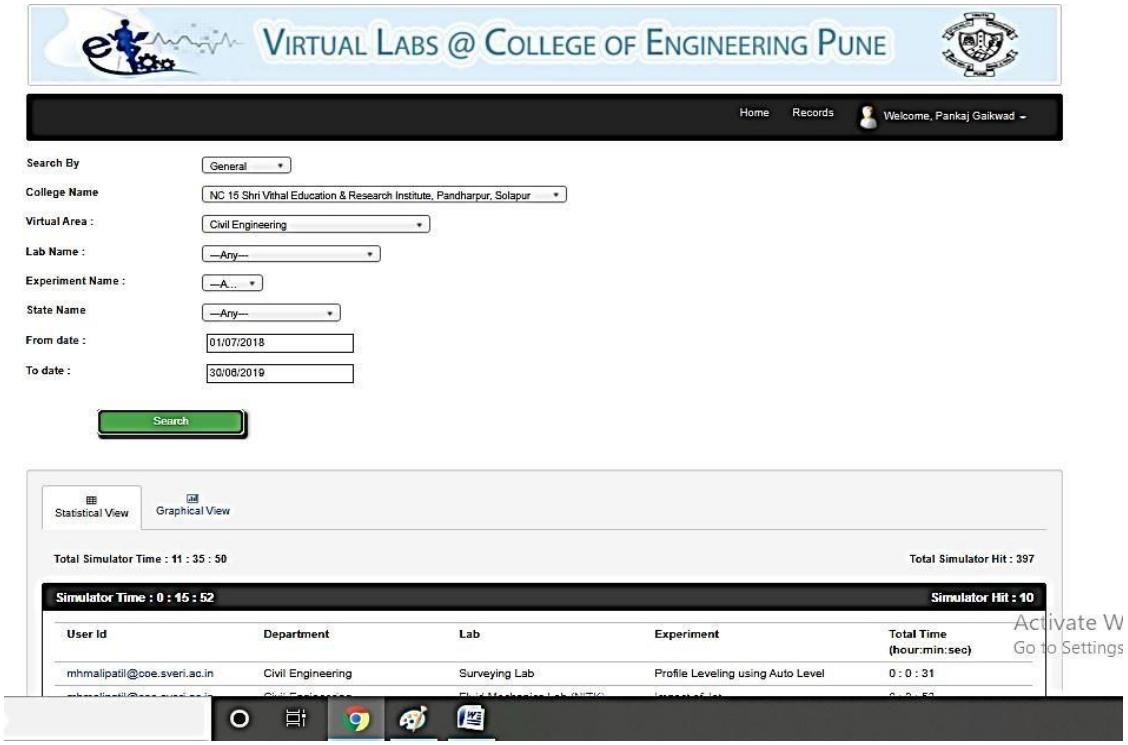


Mechanical Engineering

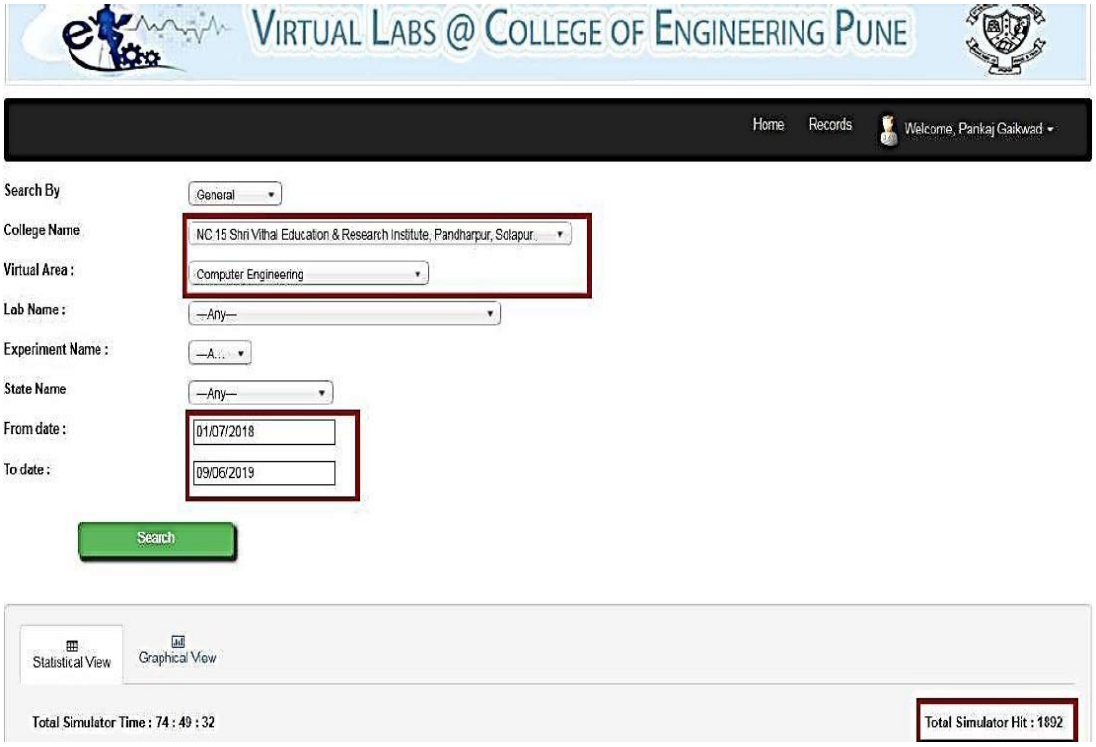


2018-19

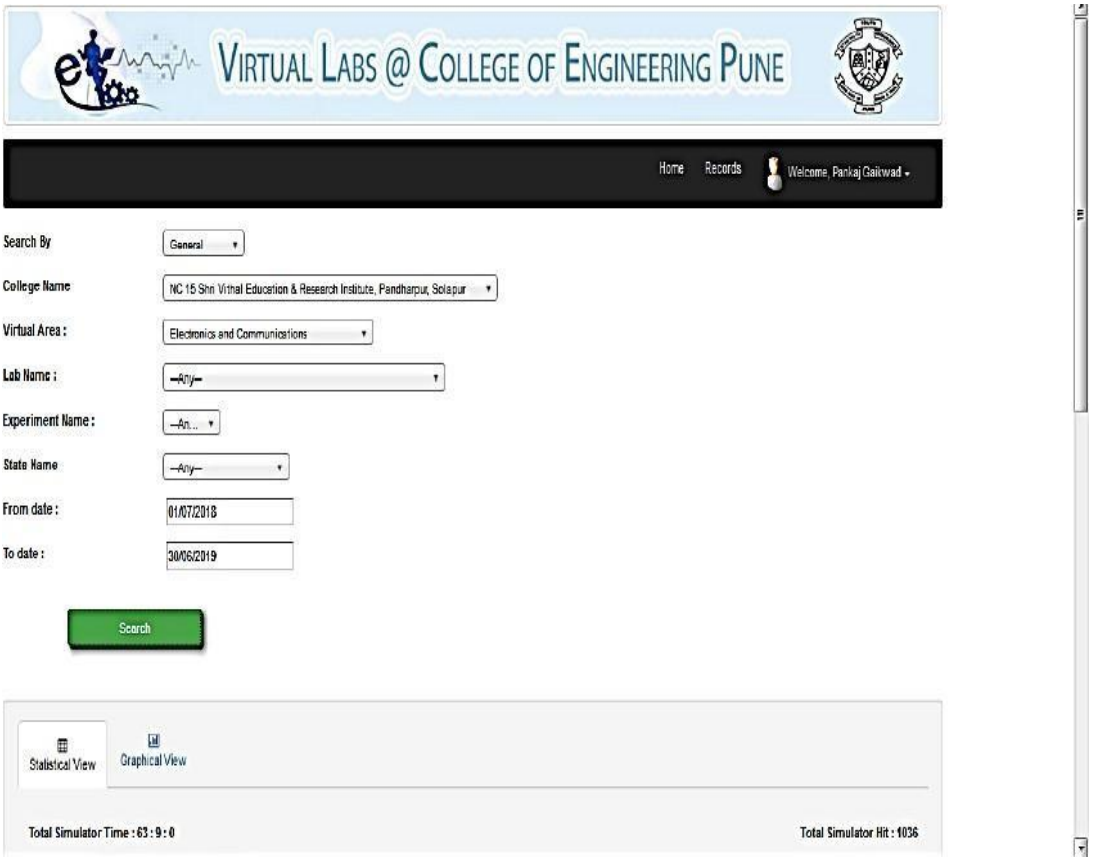
Civil Engineering



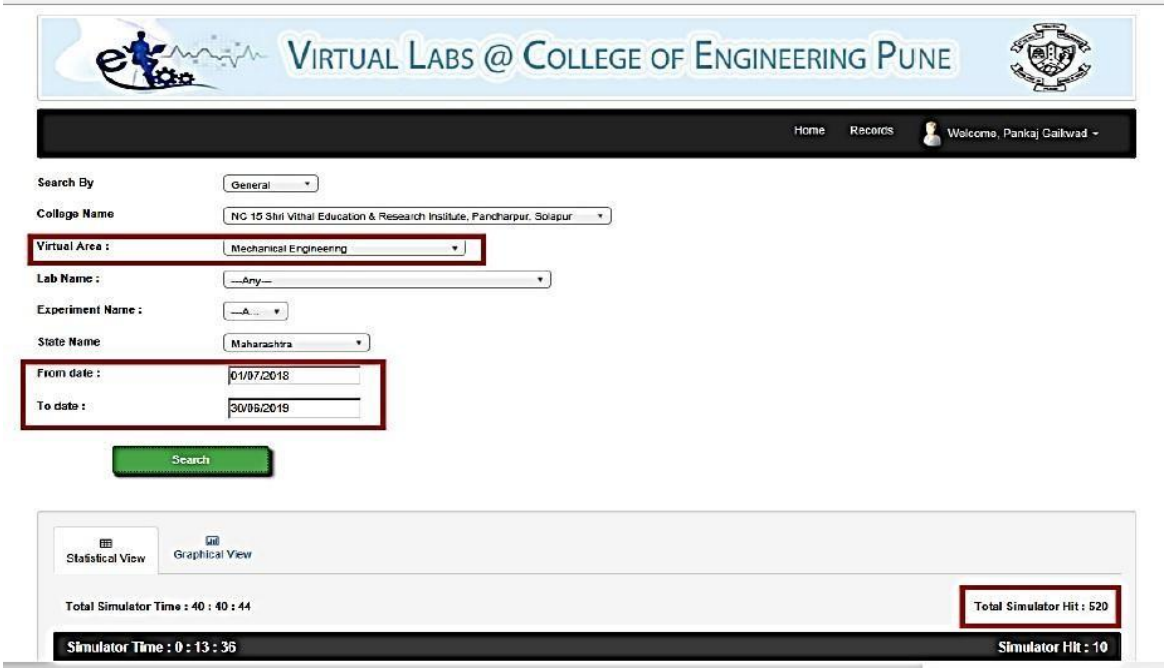
Computer Science & Engineering



Electronics & Telecommunication Engineering

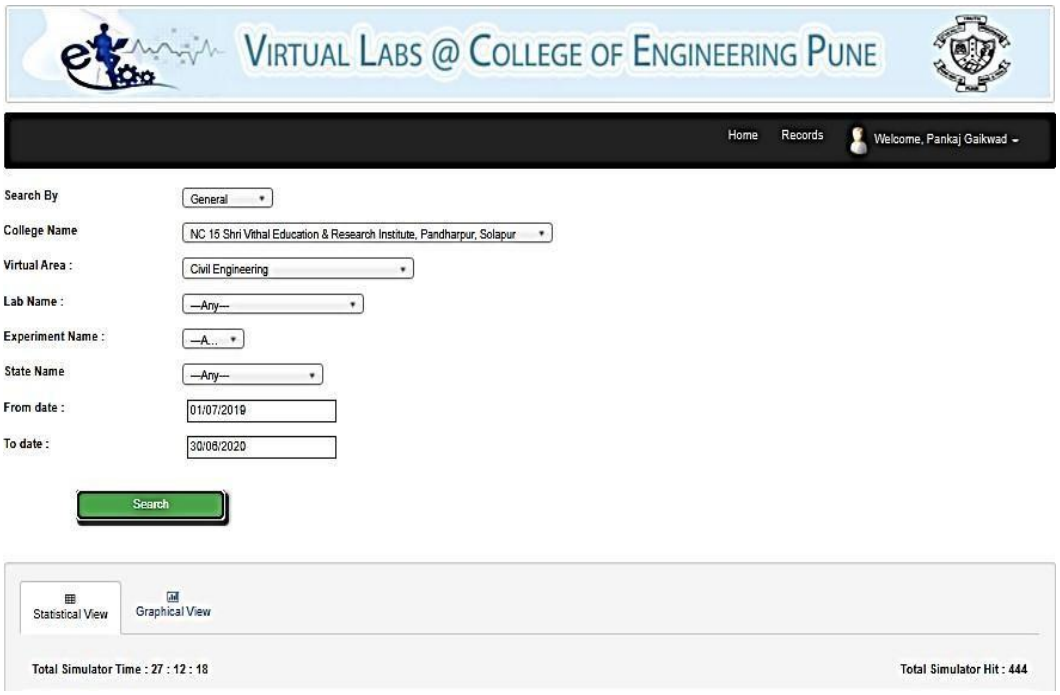


Mechanical Engineering

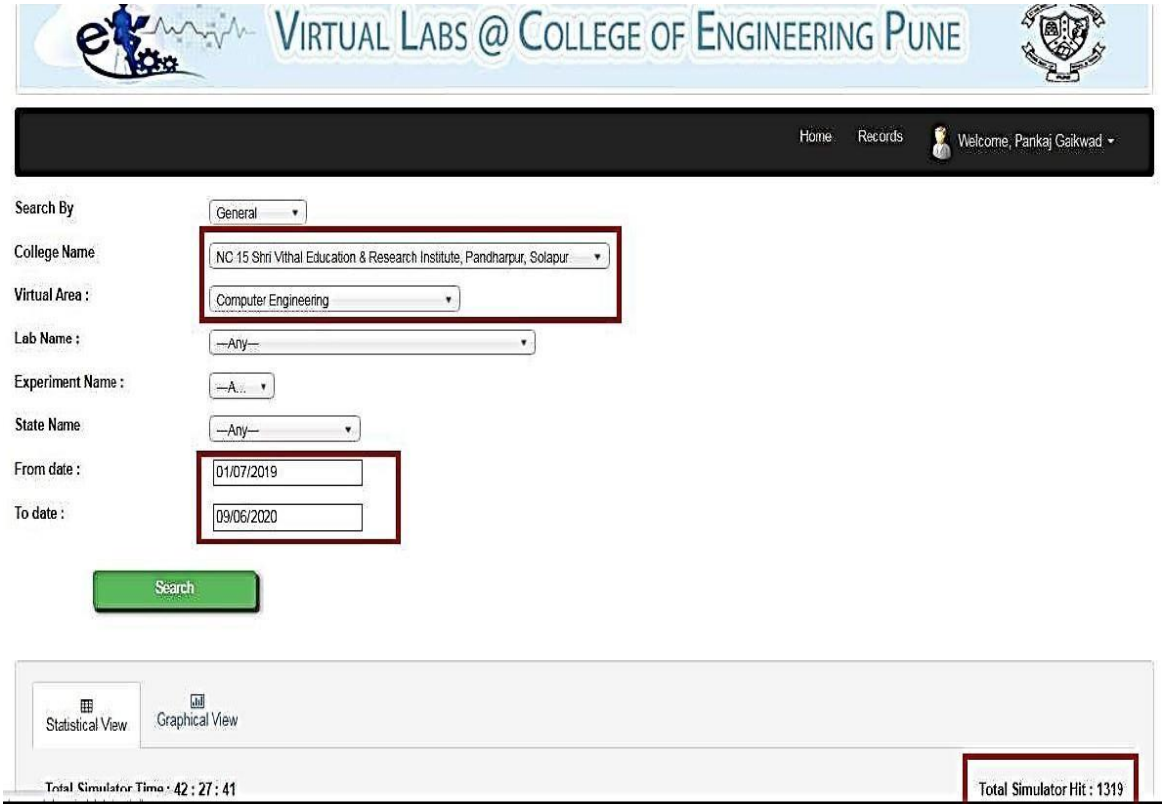


2019-20

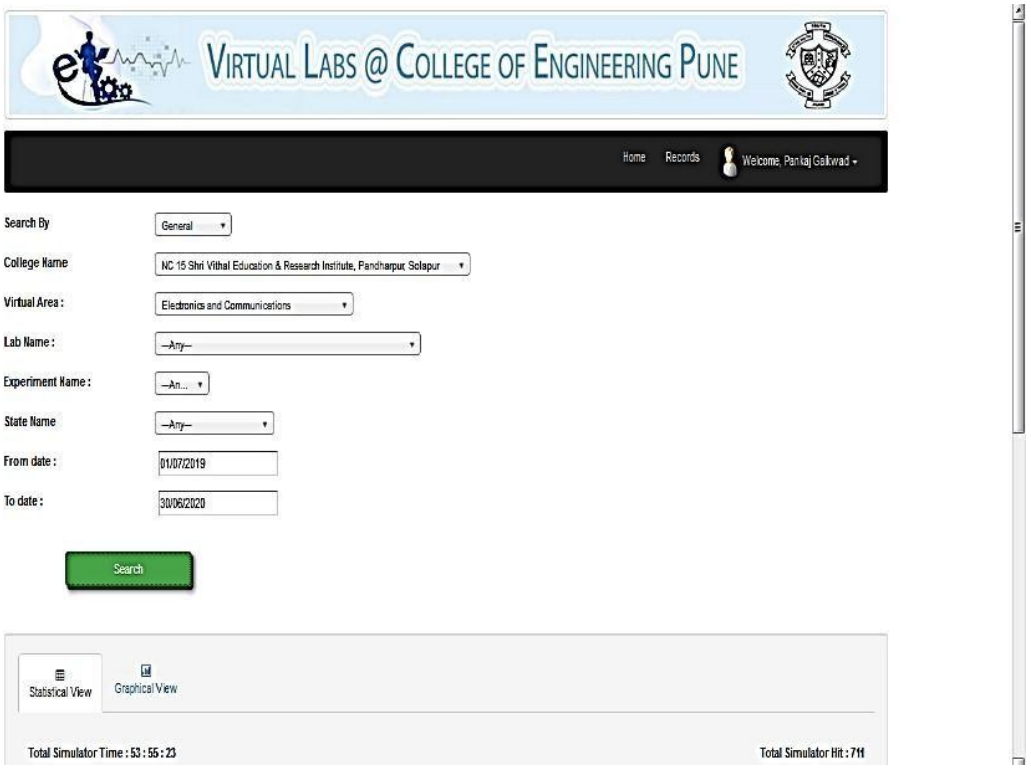
Civil Engineering



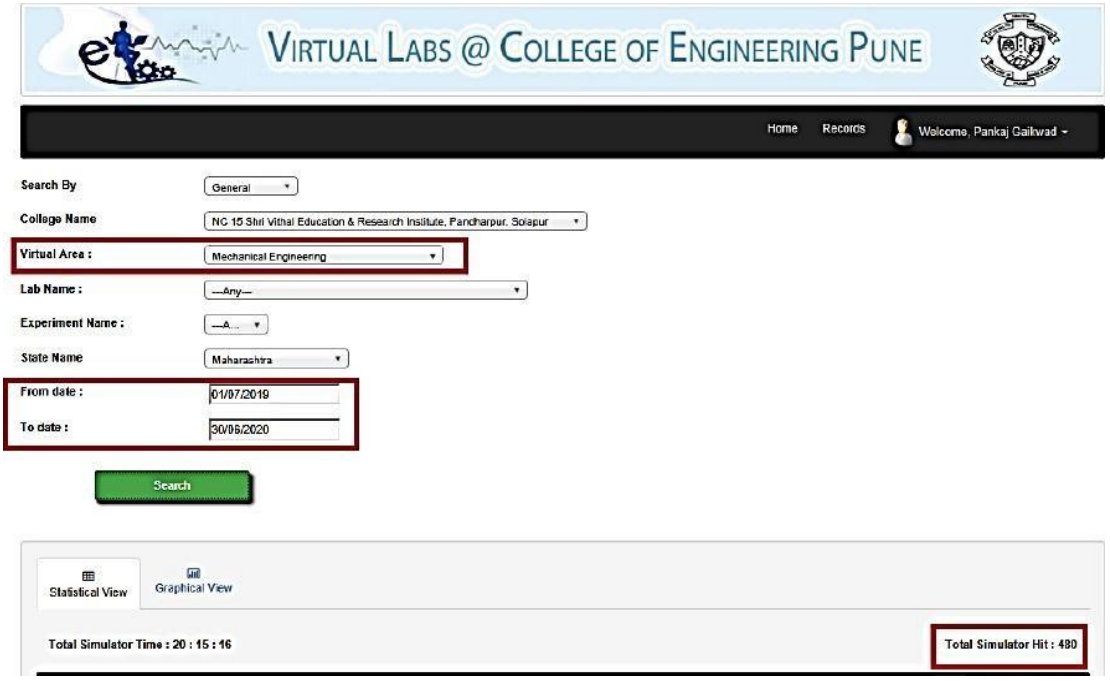
Computer Science & Engineering



Electronics & Telecommunication Engineering



Mechanical Engineering



SVNET

Sh search Institute's
C INEERING, PANDHARPUR
P.B. No. 54, Gopalpur -Ranjani Road, Gopalpur, Pandharpur- 413 304, District: Solapur (Maharashtra)Tel.: 02186-216063, 9503103757, Toll Free No.: 1800-3000-4131, E-mail: coe@sveri.ac.in,Web: www.sveri.ac.in (Approved by A.I.C.T.E., New Delhi and Affiliated to Solapur University, Solapur NBA Accredited all eligible UG Programmes, NAAC Accredited Institute, Accredited by The Institution of Engineers (India), Kolkata and TCS, Pune. ISO 9001-2015 Certified Institute

Second Year (Electronics and Telecommunication Engineering) A.Y. 2018-19

First Semester	Second Semester
• Electronics Circuit Analysis and Design-I [A] / [B]	• Linear Integrated Circuits [A] / [B]
• Circuits and Network [A] / [B]	• Electronic Circuit Analysis and Design-II [A] / [B]
• Digital Techniques [A] / [B]	• Data Structures [A] / [B]
• Analog Communication [A] / [B]	• Control Systems [A] / [B]
• Electronics Software Lab-I [A] / [B]	• Signals and Systems [A] / [B]
• Engineering Mathematics-III [A] / [B]	• Electronics Software Lab-I [A] / [B]

Third Year (Electronics and Telecommunication Engineering) A.Y. 2018-19

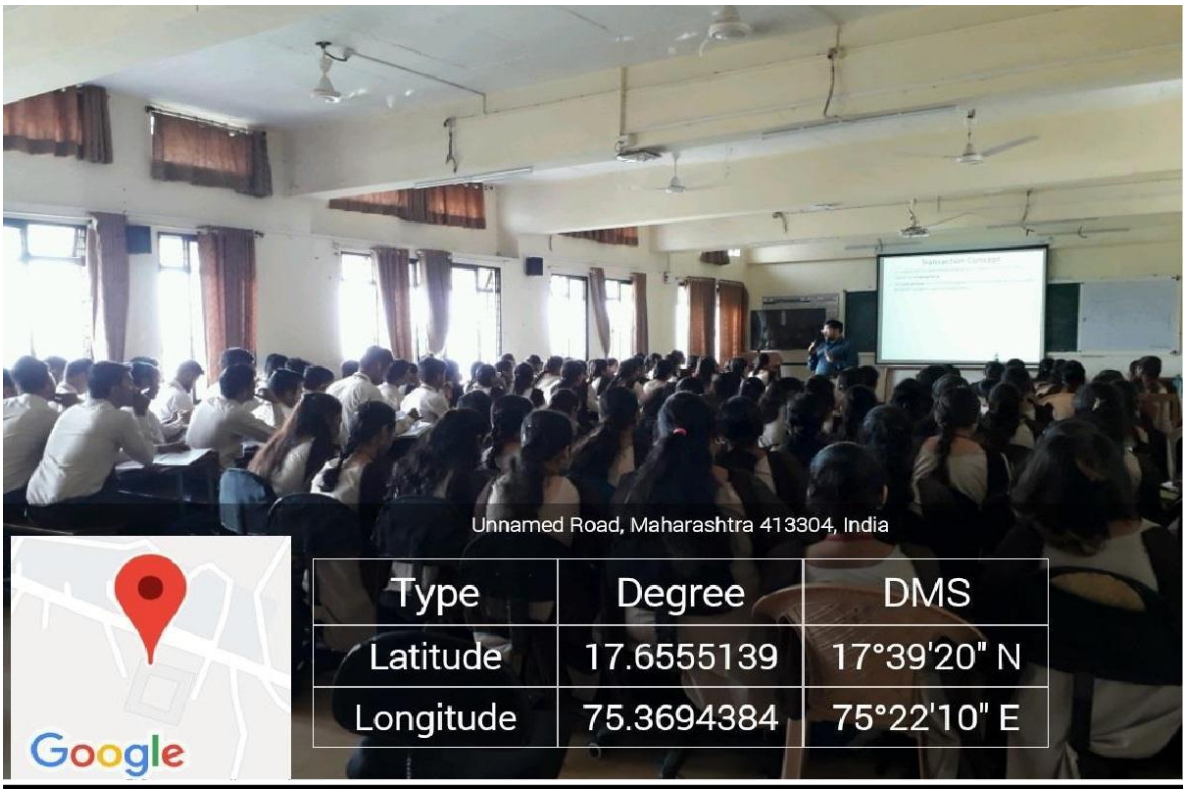
First Semester	Second Semester
• Electro Magnetic Engineering & Radiating System [A] / [B]	• Radar & Microwave Engineering [A] / [B]
• Principles of Digital Communication [A] / [B]	• Microcontrollers & Applications [A] / [B]
• Software Engineering & Project Management System [A] / [B]	• Electronics Applications &System Design [A] / [B]
• Digital Signal Processing [A] [lab] / [B]	• Optical Communication [A] / [B]
• Microprocessors [A] / [B]	• Mobile Communication [A] / [B]
• Electronic Software Lab-II [A] / [B]	• Mini Project (Hardware) [A] / [B]

Final Year (Electronics and Telecommunication Engineering) A.Y. 2018-19

First Semester	Second Semester
• Computer Communication Network [A] / [B]	• Broadband Communication [A] / [B]
• VLSI Design [A] / [B]	• Multimedia Communication Techniques [A] / [B]
• Satellite communication [A] / [B]	• Embedded Systems [A] / [B]
• Coding Theory [A] / [B]	• Elective – II (Pattern Rec.) [A] / [B]
• Elective – I (Image Processing)[A] / [B]	• Wireless Sensor Network [A]/[B]
	• Elective – II (Internet of Things) [A] / [B]

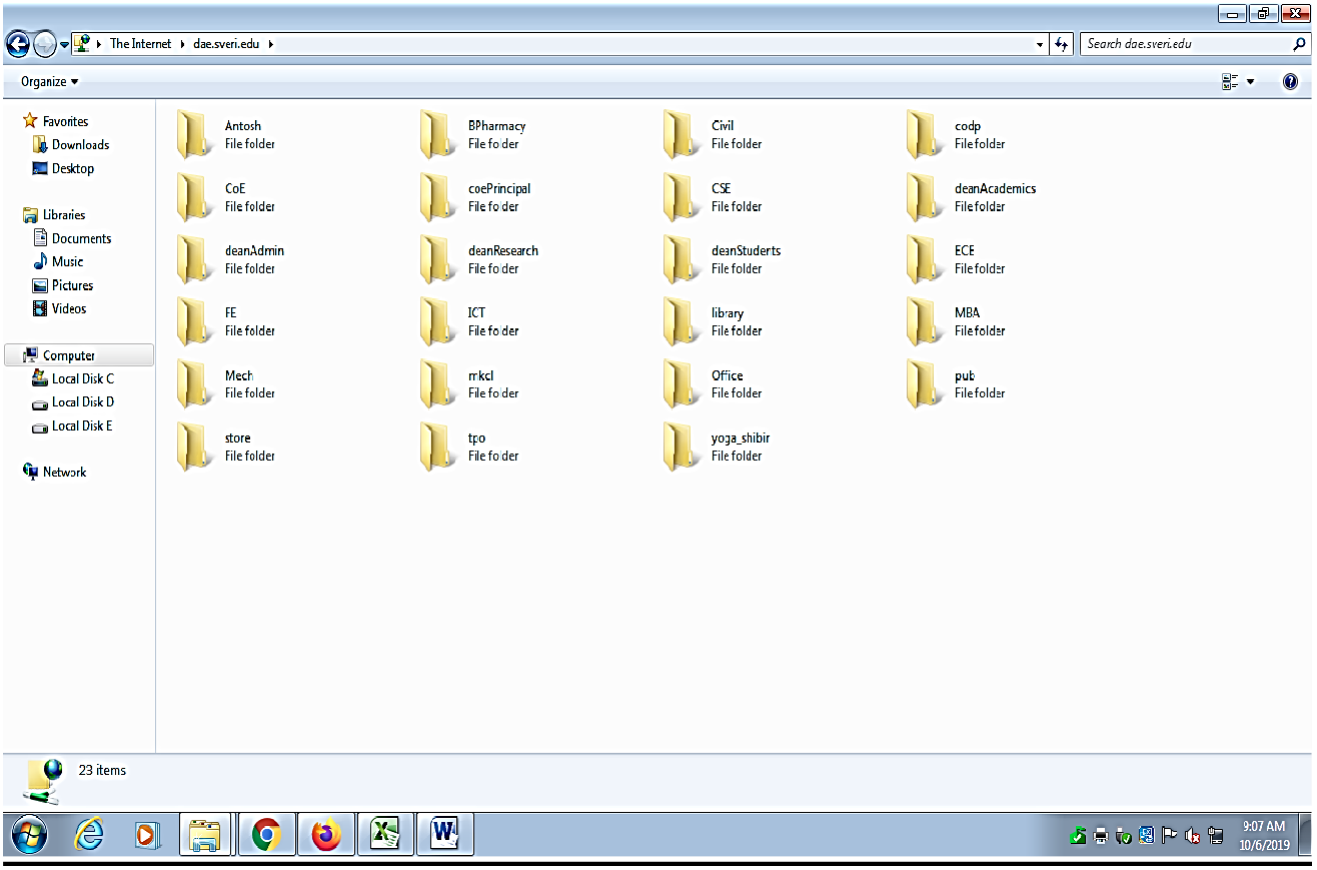
Second Year	
First Semester	**Second Semester**
• Analysis of Mechanical Elements • Applied Thermodynamics • Manufacturing Processes • Engineering Mathematics-III • Machine Drawing • CPP	• CAMD • EET • Fluid Mechanics • Theory of Machines • Numerical Methods • MTP
Third Year	
First Semester	**Second Semester**
• MMM • TOM-II • Metallurgy • Machine Design-I • Fluid Machinery & Fluid Power • ACP-I • Self Learning	• CAD CAM CAE • Tool Engineering • IC Engine • Heat and Mass Transfer • MD-II • ESA
Final Year	
First Semester	**Second Semester**
• Automatic Control Engineering • Operations Research • Refrigeration & Air Conditioning • Automobile Engineering • Entrepreneurship Development	• IQM • Industrial Engineering • Plastic Engineering • Production and Operation Management

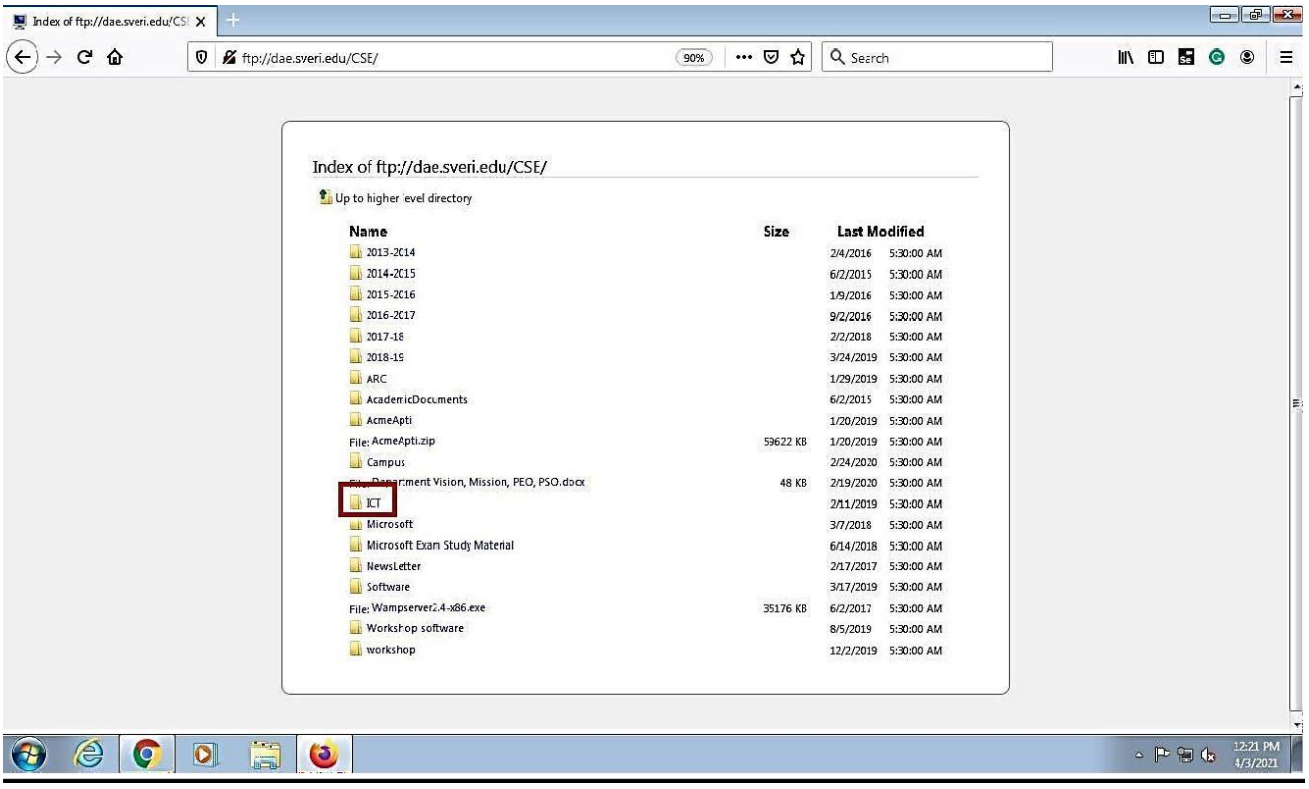
LCD PROJECTOR

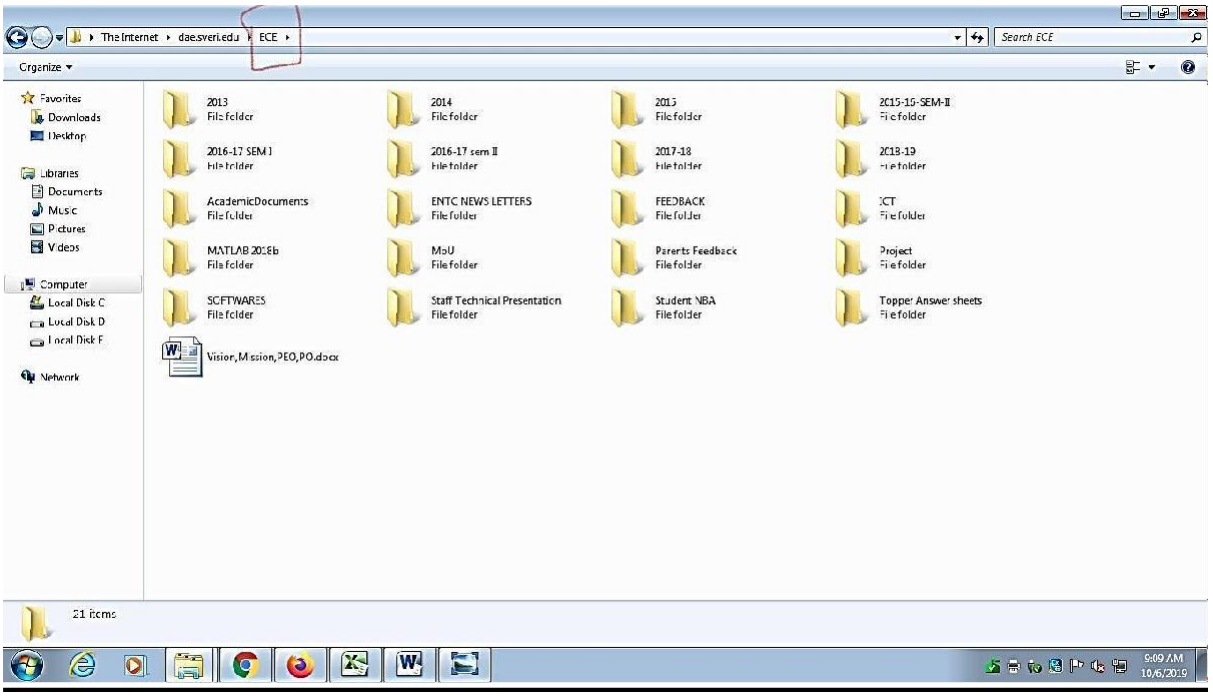


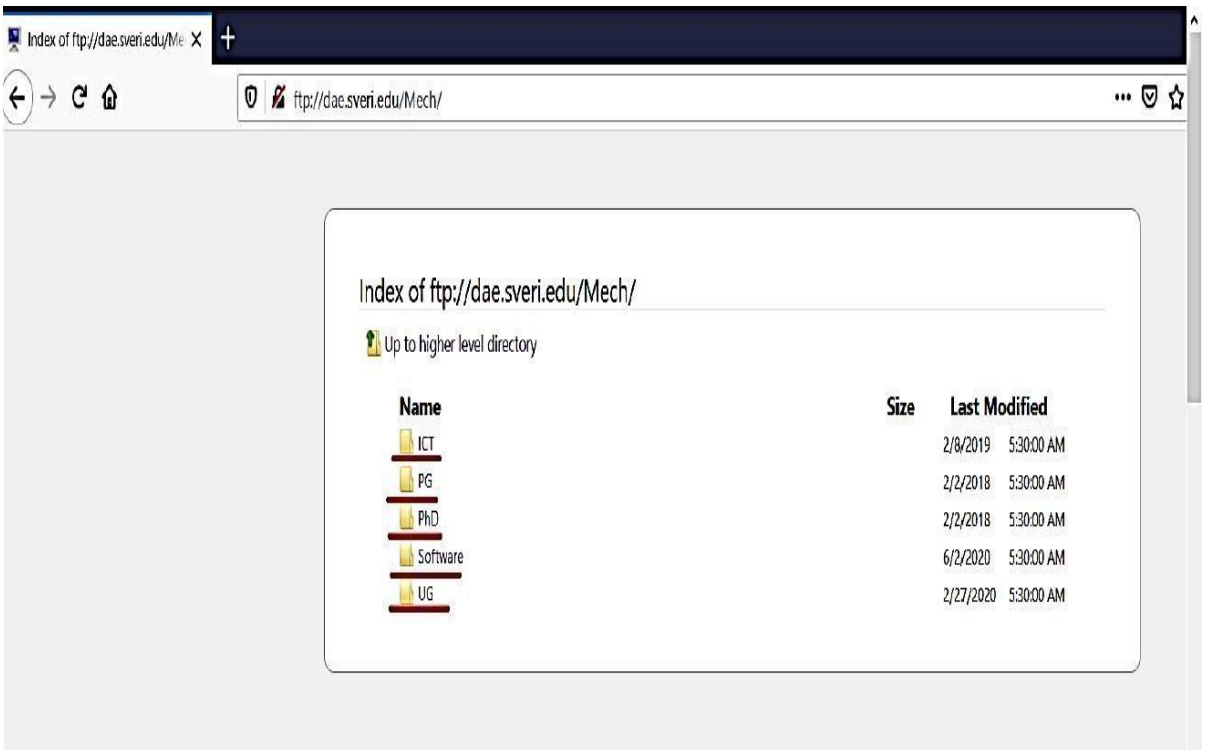
Type	Degree	DMS
Latitude	17.6555139	17°39'20" N
Longitude	75.3694384	75°22'10" E

FTP


The Internet ▸ dae.sveri.edu ▸
Search dae.sveri.edu
Organize
Favorites
Downloads
Desktop
Libraries
Documents
Music
Pictures
Videos
Computer
Local Disk C
Local Disk D
Local Disk E
Network
Antosh File folder
BPharmacy File folder
Civil File folder
codp File folder
CoE File folder
coePrincipal File folder
CSE File folder
deanAcademics File folder
deanAdmin File folder
deanResearch File folder
deanStuderts File folder
ECE File folder
FE File folder
ICT File folder
library File folder
MBA File folder
Mech File folder
rrkcl File folder
Office File folder
pub File folder
store File folder
tpo File folder
yoga_shibir File folder
23 items
9:07 AM
10/6/2019


Index of ftp://dae.sveri.edu/CSE/
ftp://dae.sveri.edu/CSE/
90%
Search
Index of ftp://dae.sveri.edu/CSE/
Up to higher level directory
Name
Size
Last Modified
2013-2C14 2/4/2016 5:30:00 AM
2014-2C15 6/2/2015 5:30:00 AM
2015-2C16 1/9/2016 5:30:00 AM
2016-2C17 9/2/2016 5:30:00 AM
2017-18 2/2/2018 5:30:00 AM
2018-19 3/24/2019 5:30:00 AM
ARC 1/29/2019 5:30:00 AM
AcademicDocuments 6/2/2015 5:30:00 AM
AcmeApti 1/20/2019 5:30:00 AM
File: AcmeApti.zip 59622 KB 1/20/2019 5:30:00 AM
Campus 2/24/2020 5:30:00 AM
48 KB 2/19/2020 5:30:00 AM
ICT 2/11/2019 5:30:00 AM
Microsoft 3/7/2018 5:30:00 AM
Microsoft Exam Study Material 6/14/2018 5:30:00 AM
NewsLetter 2/17/2017 5:30:00 AM
Software 3/17/2019 5:30:00 AM
File: Wampserver2.4-x86.exe 35176 KB 6/2/2017 5:30:00 AM
Workshop software 8/5/2019 5:30:00 AM
workshop 12/2/2019 5:30:00 AM
12:21 PM
4/3/2021

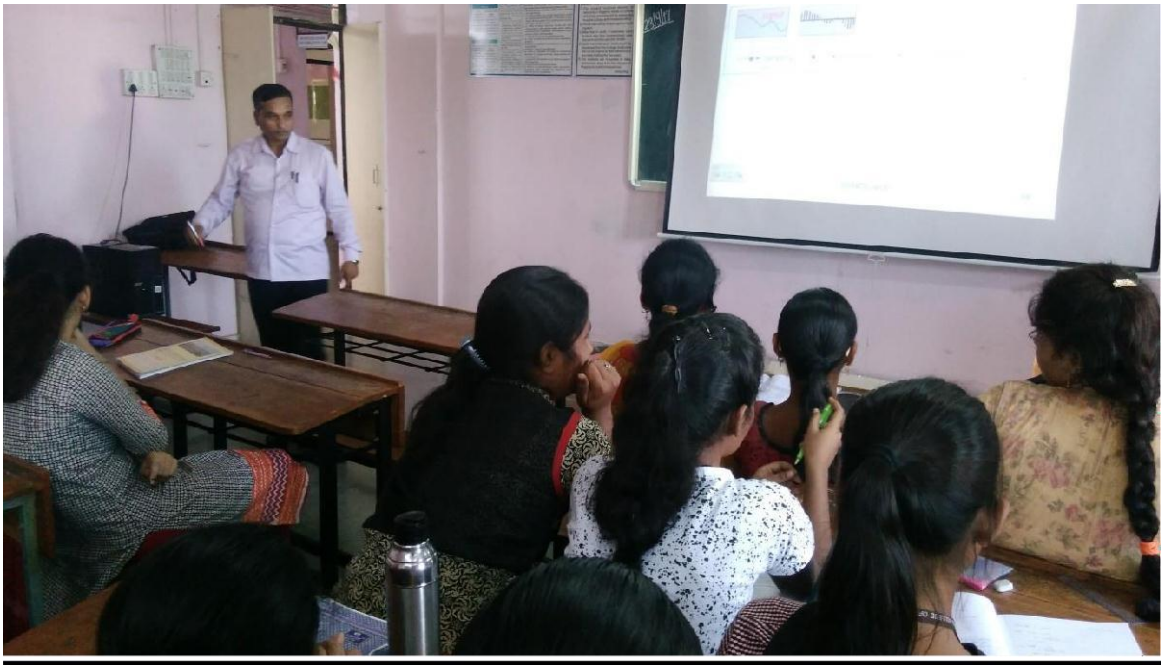

The Internet ▸ dae.sveri.edu ▸ ECE ▸
Search ECE
Organize
Favorites
Downloads
Desktop
Libraries
Documents
Music
Pictures
Videos
Computer
Local Disk C
Local Disk D
Local Disk F
Network
2013
File folder
2014
File folder
2015
File folder
2015-16-SEM-II
File folder
2016-17 SEM I
File folder
2016-17 sem II
File folder
2017-18
File folder
2018-19
File folder
AcademicDocuments
File folder
ENTC NEWS LETTERS
File folder
FEEDBACK
File folder
ICT
File folder
MATLAB 2018b
File folder
MoU
File folder
Parents Feedback
File folder
Project
File folder
SOFTWARES
File folder
Staff Technical Presentation
File folder
Student NBA
File folder
Topper Answer sheets
File folder
Vision, Mission, PEO, PO.docx
21 items
9:09 AM
10/6/2019


Index of ftp://dae.sveri.edu/Me
ftp://dae.sveri.edu/Mech/
Index of ftp://dae.sveri.edu/Mech/
Up to higher level directory
Name
Size
Last Modified
ICT
2/8/2019 5:30:00 AM
PG
2/2/2018 5:30:00 AM
PhD
2/2/2018 5:30:00 AM
Software
6/2/2020 5:30:00 AM
UG
2/27/2020 5:30:00 AM

PPT

PPT LINKS
https://drive.google.com/file/d/1RSenJjQ96pY8pc0KARa0SbBp1FsW8-L4/view?usp=sharing
https://drive.google.com/file/d/15rX5BKxj7j2VmrsR_6GU8WLovmvKPR2S/view?usp=sharing
https://drive.google.com/file/d/14YcJJN81o0VsvEpMOCb7b_tOqoHO7n0X/view?usp=sharing
https://drive.google.com/a/coe.sveri.ac.in/file/d/10bCAT9k_BzB_Jto_NBNwW1VgHW0g4kM8/view?usp=drivesdk
https://drive.google.com/a/coe.sveri.ac.in/file/d/10bCAT9k_BzB_Jto_NBNwW1VgHW0g4kM8/view?usp=drivesd

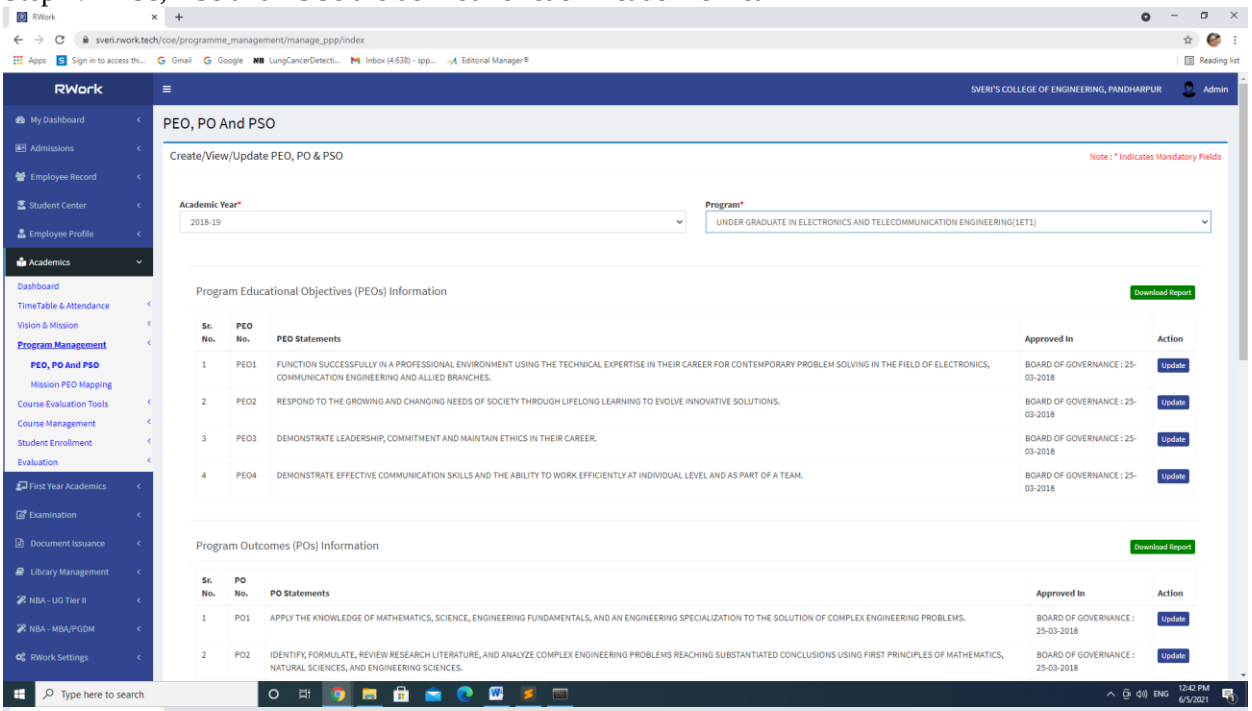

Object recognition and classification
Pattern vectors. Historical example: Recognition of three types of iris flowers by the lengths and widths of their petals (Fisher 1936).
Variations between and within classes.
Class seperability depends strongly on the choice of descriptors.
Shape signature represented by the sampled amplitude values. Cloud of n-dimensional points.
Other shape characteristics could have been employed (e.g. moments).
The choice of descriptors has a profound role in the recognition performance.

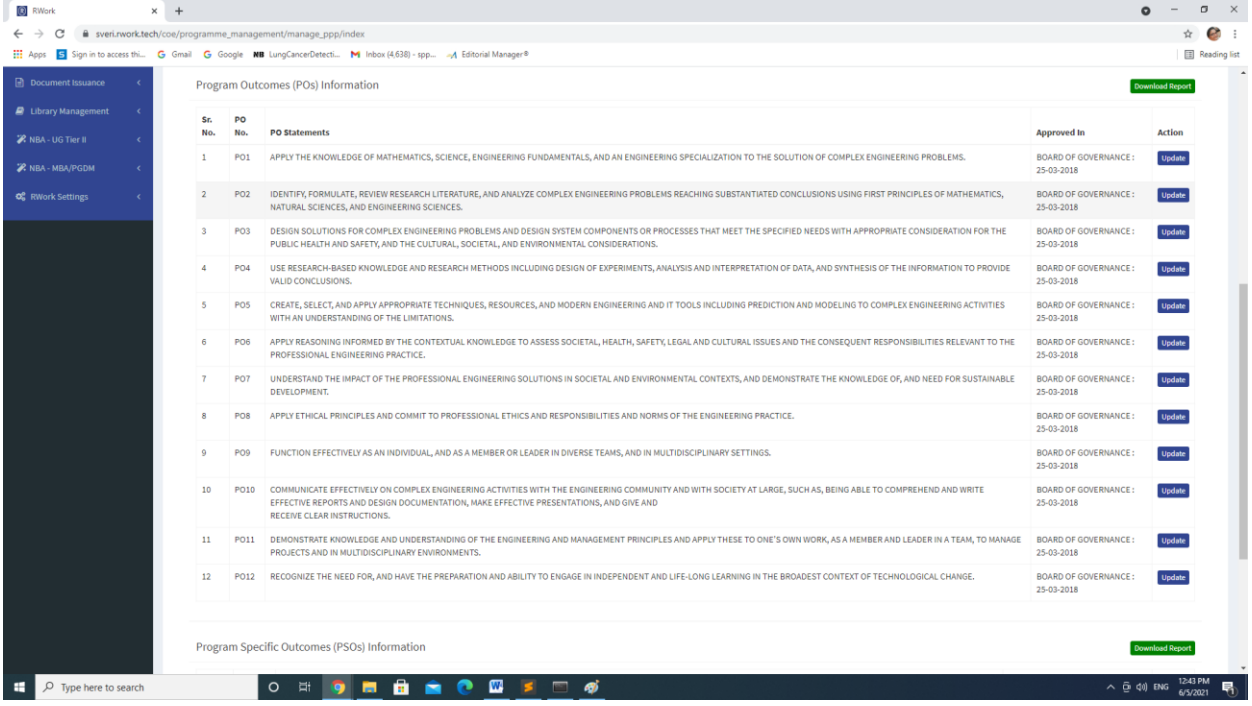


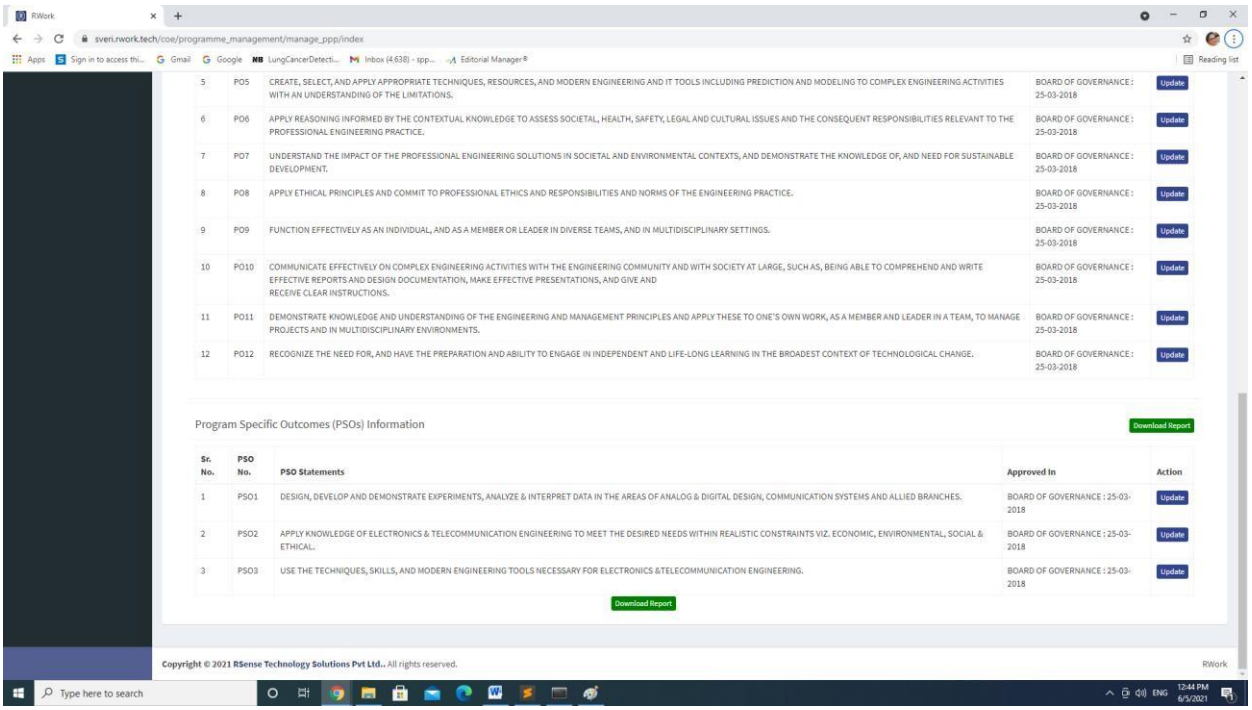
ERP-based CO-PO Attainment

Outcome Based Education (OBE) System is implemented through RWork (ERP) software and is explained using a course Electro Magnetic Engg. & Radiating System (ET311-18) of Third Year under Program UG in Electronics and Telecommunication Engineering as follows

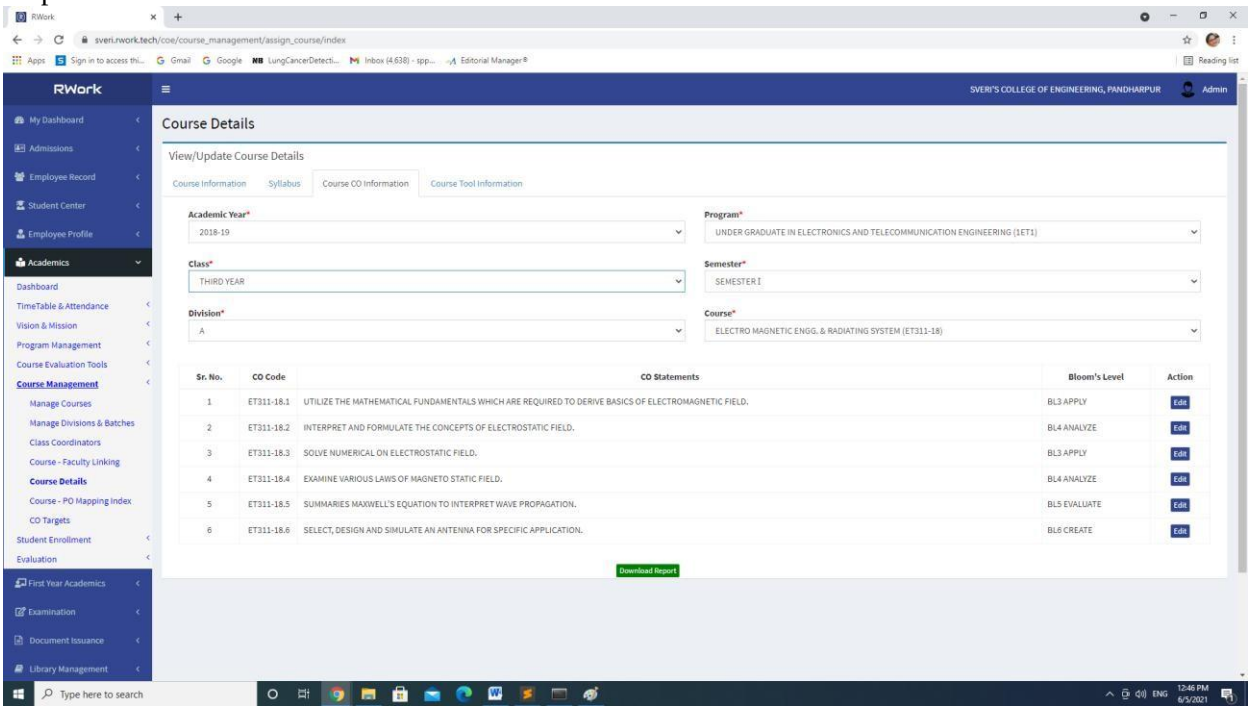
Step 1: PEOs, POs and PSOs are defined for each Academic Year



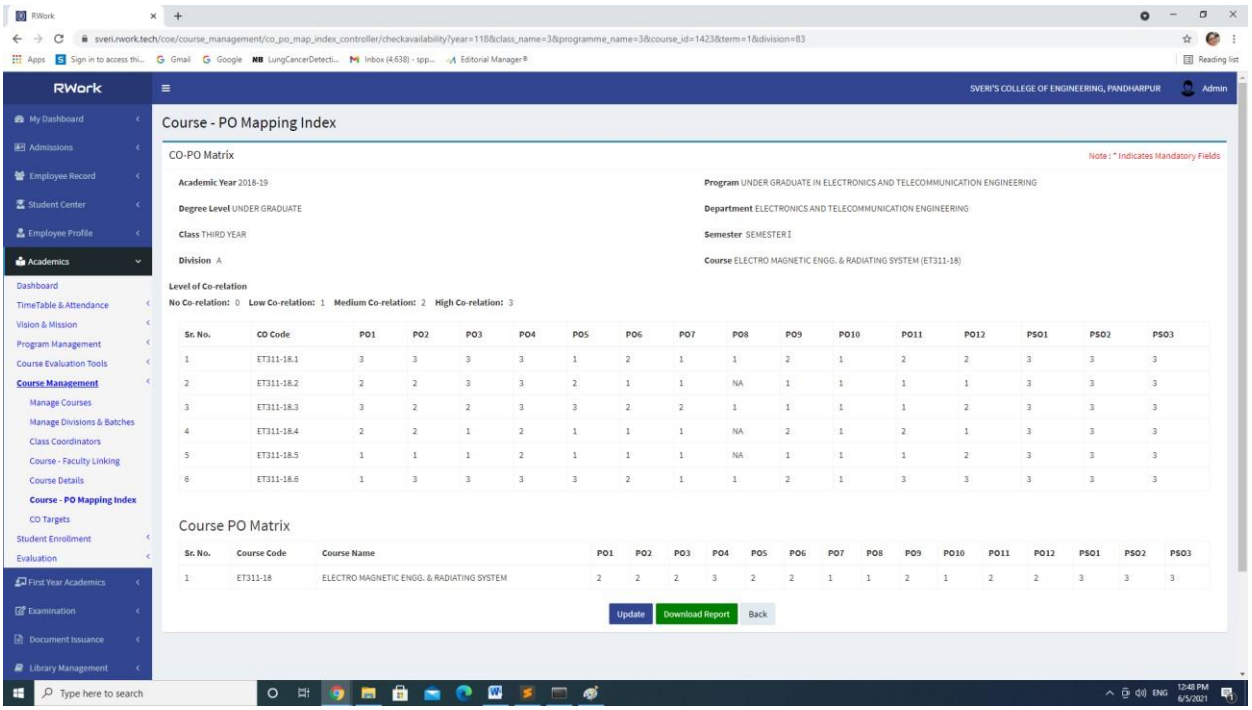




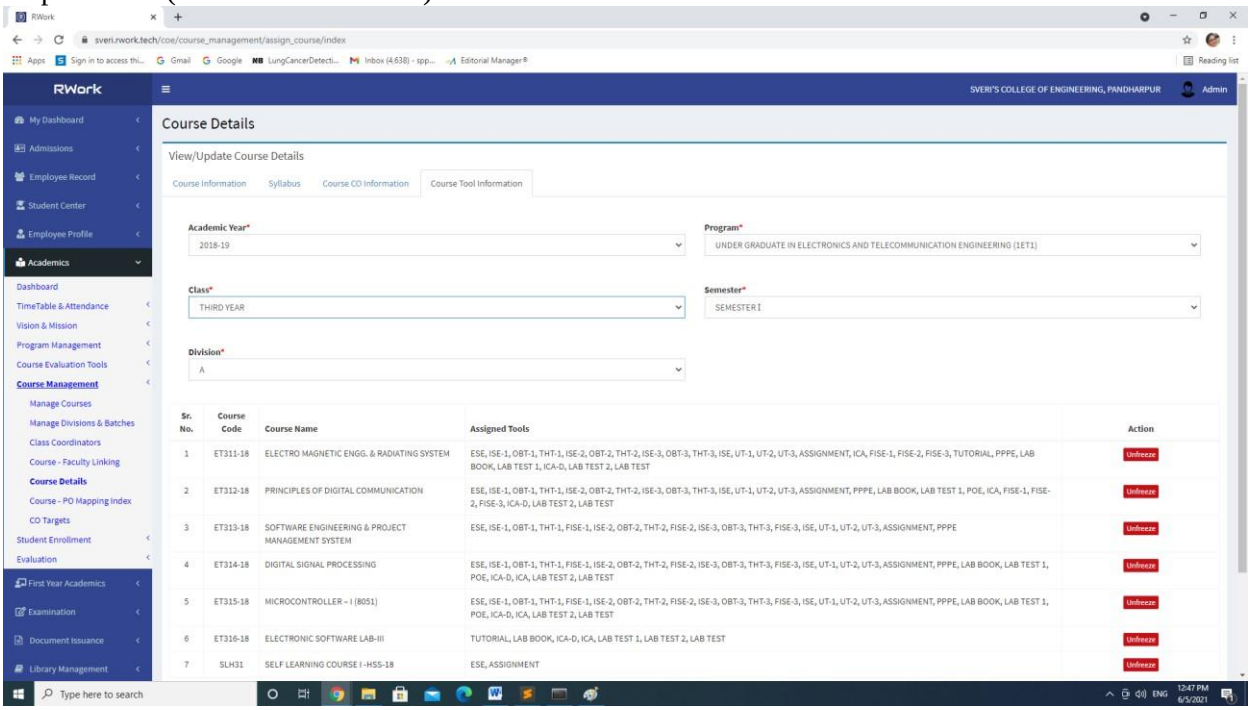
Step 2: COs are defined for a course



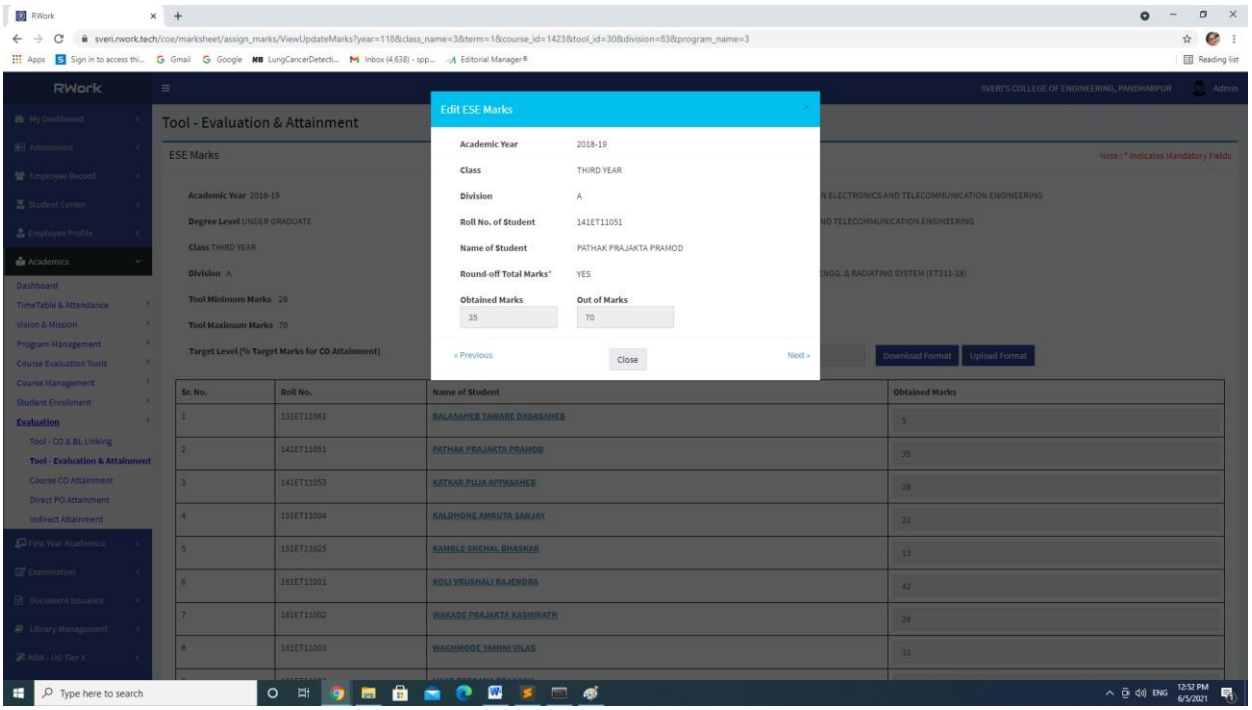
Step 3: CO-PO mapping index is prepared



Step 4: Tools (Internal and External) are defined for CO Attainment

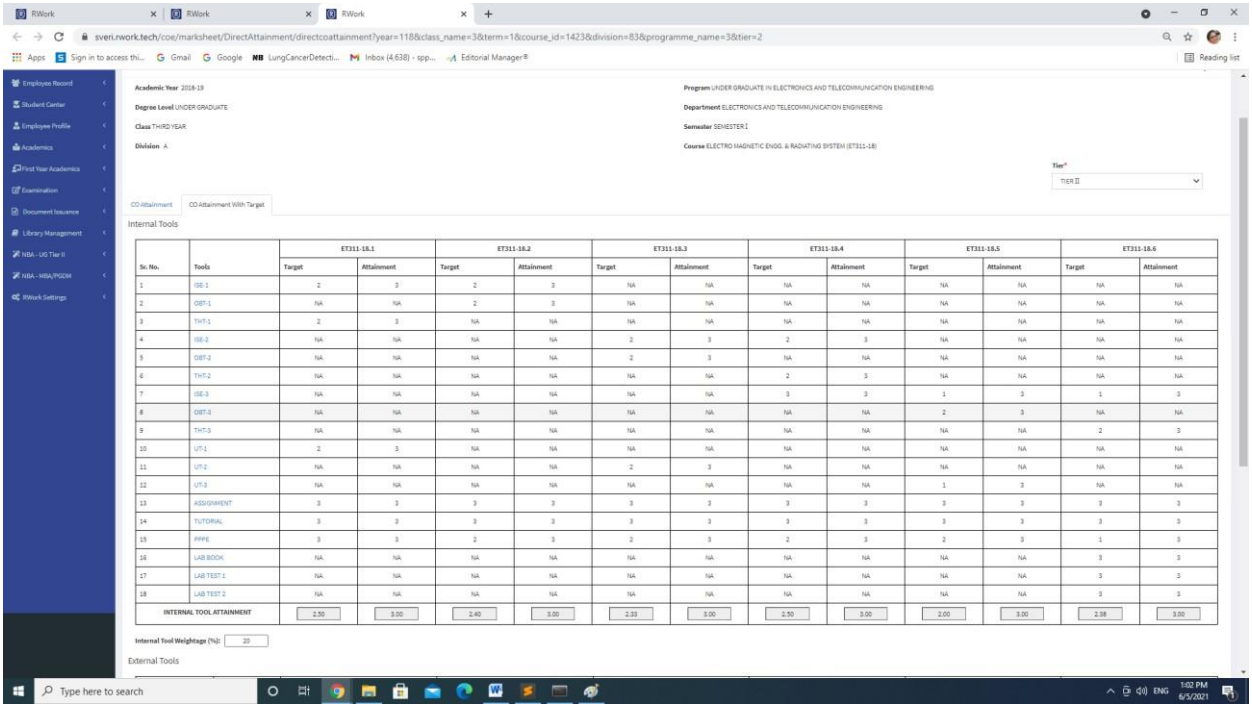


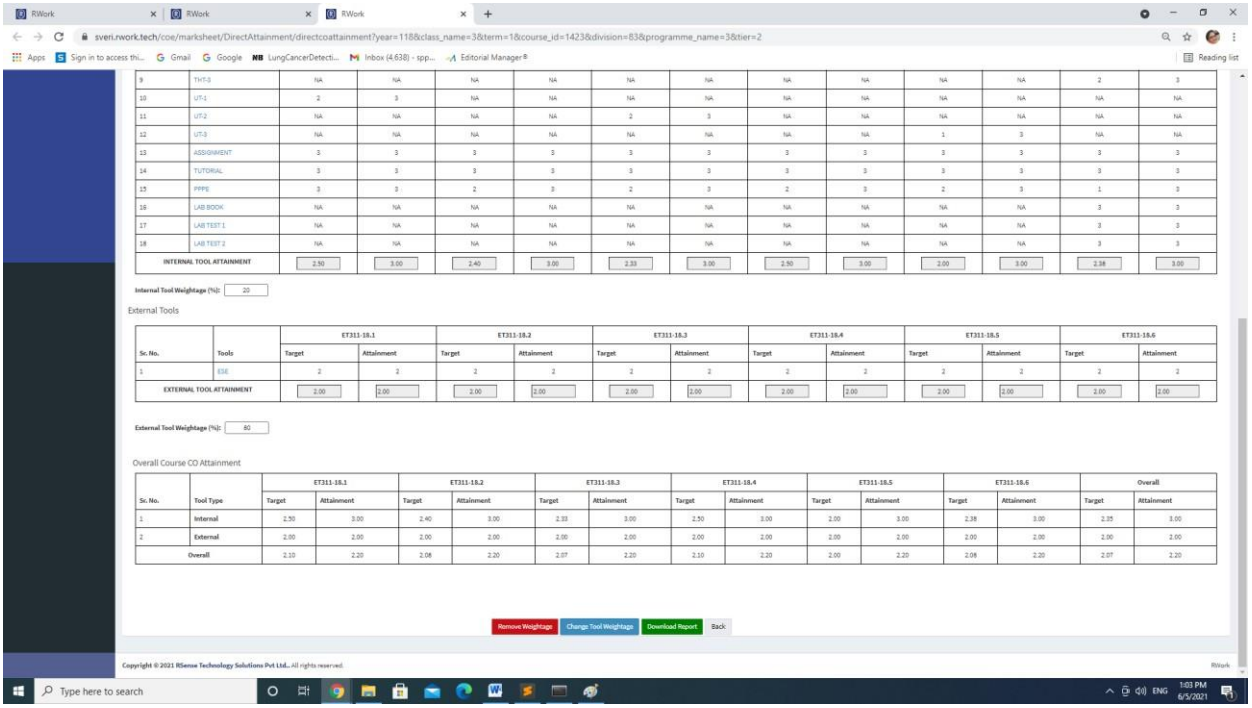
Step 5: Marks filling process is performed in defined Tools
Link for Excel sheet:



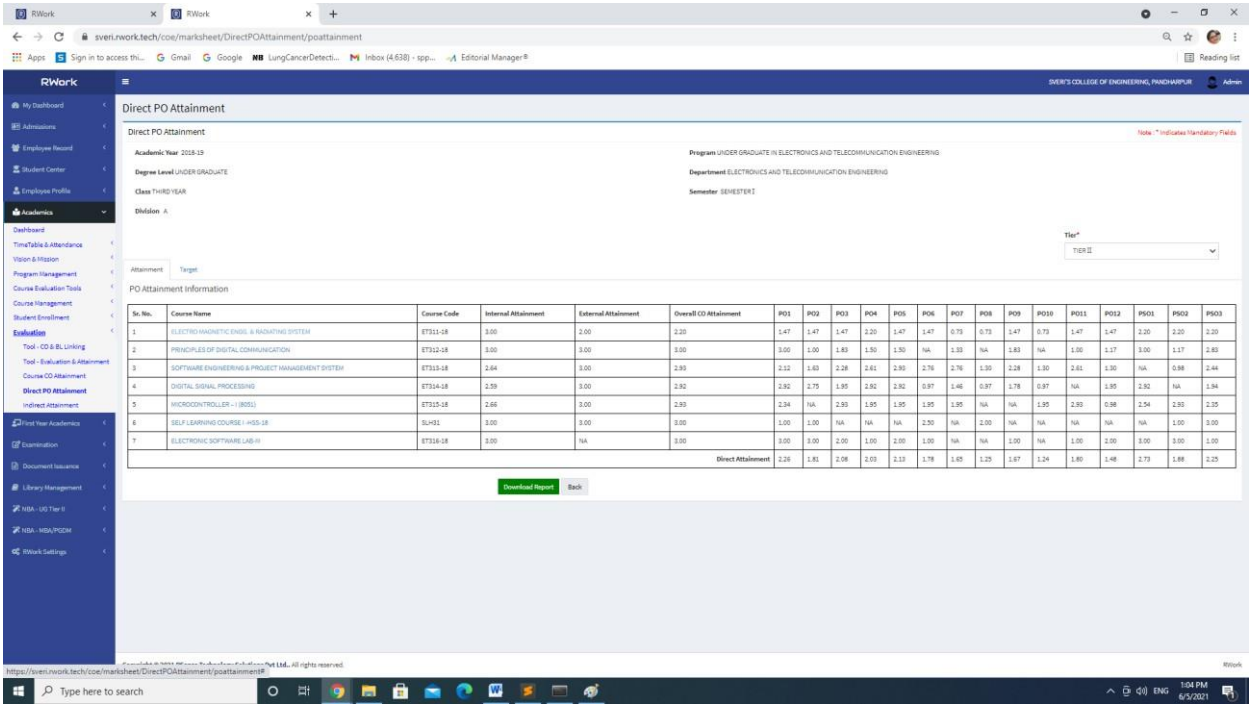
Step 6: CO attainment of all Tools (Internal & External)

Link for Excel sheet:

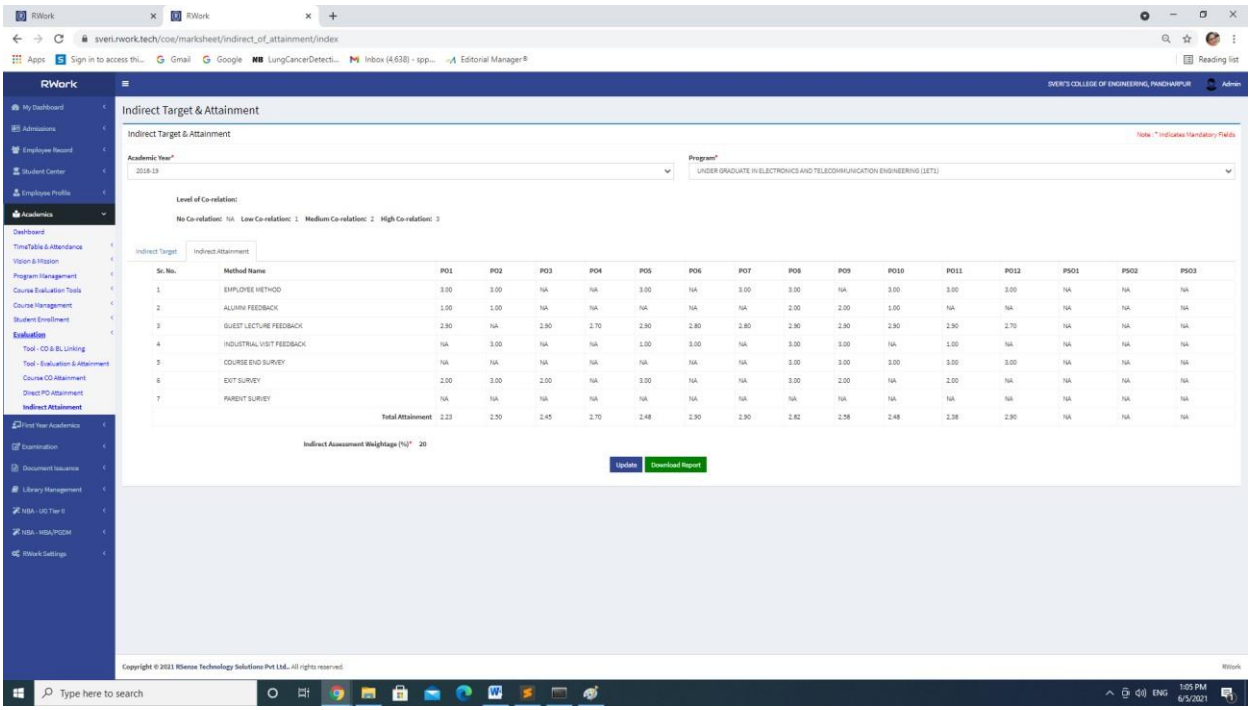




Step 7: Direct PO attainment of a course
Link for Excel sheet:



Step 8: Indirect PO attainment
Link for Excel sheet:



Sr. No.	Method Name	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	EMPLOYEE METHOD	3.00	3.00	NA	NA	3.00	NA	3.00	3.00	NA	3.00	3.00	3.00	NA	NA	NA
2	ALUMNI FEEDBACK	1.00	1.00	NA	NA	NA	NA	NA	2.00	2.00	1.00	NA	NA	NA	NA	NA
3	GUEST LECTURE FEEDBACK	2.90	NA	2.90	2.70	2.90	2.80	2.80	2.90	2.90	2.90	2.90	2.70	NA	NA	NA
4	INDUSTRIAL VISIT FEEDBACK	NA	3.00	NA	NA	1.00	3.00	NA	3.00	3.00	NA	1.00	NA	NA	NA	NA
5	COURSE END SURVEY	NA	NA	NA	NA	NA	NA	NA	3.00	3.00	3.00	3.00	3.00	NA	NA	NA
6	EXIT SURVEY	2.00	3.00	2.00	NA	3.00	NA	NA	3.00	2.00	NA	2.00	NA	NA	NA	NA
7	PARENT SURVEY	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA
	Total Attainment	2.23	2.50	2.45	2.70	2.48	2.90	2.90	2.82	2.58	2.48	2.38	2.90	NA	NA	NA

Indirect Assessment Weightage (%)* 20