

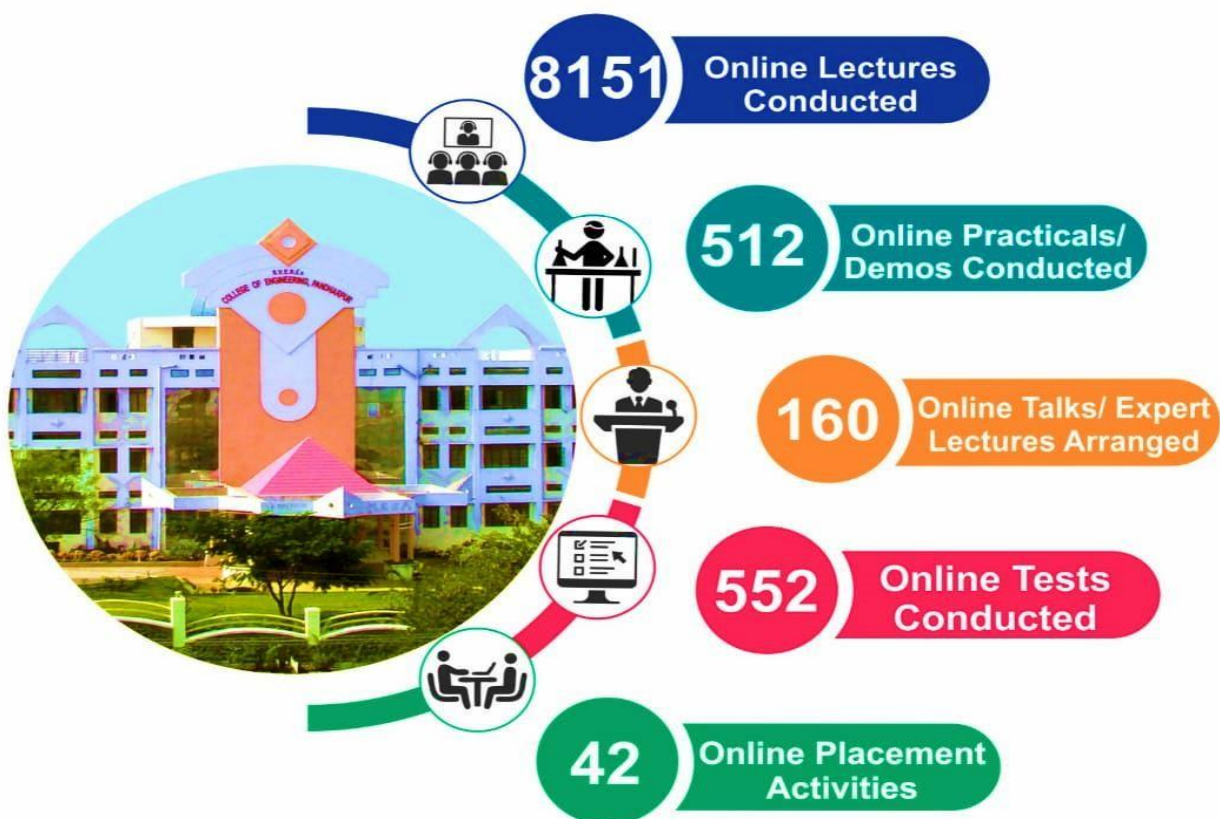
ICT TOOLS

In a recent pandemic situation, all the academic activities were conducted online. The summary of these activities is as below:



SVERI's College of Engineering, Pandharpur

Online Academic Activities : Semester- I



**ADVANCED TECHNICAL
TRAINING (ATT)**

The purpose of the Advanced Technical Training (ATT) is to provide the knowledge of all technical subjects in Engineering & Science. Further, it gives clarity about the important questions asked in competitive exams like IES, SSC, JE, RTO, Indian Navy etc. Sample link of ATT activity is given below:

DEPARTMENT OF CIVIL ENGINEERING
https://att.sveri.ac.in/index.php
DEPARTMENT OF MECHANICAL ENGINEERING
https://att.sveri.ac.in/mech.php
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING
https://att.sveri.ac.in/ece.php
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
https://att.sveri.ac.in/cse.php
DEPARTMENT OF ELECTRICAL & ENGINEERING
https://att.sveri.ac.in/ee.php

VIDEO LECTURE LINKS

Class	Link
DEPARTMENT OF CIVIL ENGINEERING	
SY	https://docs.google.com/spreadsheets/d/e/2PACX-1vTD9H7mFkMsOBLzPPLD9vLMdZhRTWNggBFI5cZ4zvnjsrkECUTiYrEzuHn1Zp9Tl_yu_jeATUxGohXvD/pubhtml
TY-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vSocMr7MoWPWjwEfNGKP3a6qemX6t4C2P2gbWX2AGZ8CNIQErZn3BrYuDHRqu8_2ZcYQA4RW4T3XuKv7/pubhtml
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRqQyRKfNTBvx5kX-xyzrS2TrFD7L-QdWsXd8zQn4IlQoqwu7PQ-JWKILz2KGiLO9d3U2fGLkFUfe6X/pubhtml
BE	https://docs.google.com/spreadsheets/d/e/2PACX-1vTJrkCluB0qt1ajrrE7YGrgLda6lQIR3MDXw0NqoeOLEiLuouiBbivEgjeB9RJ9Qb7nr6Q_CWfUsB-4p/pubhtml
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING	
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRmBwgbIU1K4KXnJa4gDih32kBLfT1NJ5g0vmlGSeNl-2wr9UY635tayofN3asiPYFcZKjfaZnfBnt3/pubhtml
BE-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vT1Af7Mkt_4PZDX6OsaS1_h6wh-PBdXC4VNEvrBauQm2OjzKXakZmj1TfEYnMCRH16DmGSGQOJOEDGu/pubhtml
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING	
SY-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vSEEAyZbwTbSoct-zfDmpN8G0Kp7N0cYaziiGH86OHmkd0pWvmCzi9mzc4lKDr3kg/pubhtml
SY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRXkr5TIQtPSBFMIKRtc4zPQKvc9FkGHcotHUYstPfHlFCBCCNq4s2CeRCQtOjZgVl_mam-845srOmtz/pubhtml
TY-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vQwBg2DSuqFW9rA7Bno94FQzpuJrg0X5sEKDFKJST6TDspleqXnXzwVBANIHJkrQ_A/pubhtml
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vSDbiSf8pB9Cu4r1QX_2PeRi-Nz_60y3SeRpfQ7iiEPsDoMVchpKART8-BLzA9L_yg/pubhtml
BE-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vTVpYN_NoThor2iFoYYVHMHXRW_QqEX-bNvKxZ_qoxsH12XaPSEqd0m4T0oUW2Svw/pubhtml
BE-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vRCtVNHecnzG8rBUzM_tky3N77Zf7y8gKmq1oPoS9EwLGMhK6hJURll9y3cRM16R_Q/pubhtml
DEPARTMENT OF MECHANICAL ENGINEERING	
SY-B	https://docs.google.com/spreadsheets/u/1/d/e/2PACX-1vQXgVqlhTAgzPswW9dlDQETLVS4M26WLHZW5ipyAJ3Zy6n6OnjmT2o4D1ladLIL_WQfDrUWUyILRJhRE/pubhtml
TY-B	https://docs.google.com/spreadsheets/d/e/2PACX-1vQ0JvIpLR5QCnRmMI7CBoV1UYf7-feqrlVTXkeO1CC0X0YGpzSw7w0oZGfacpvvZjJcWsS5E_VsRIZoA/pubhtml
BE-A	https://docs.google.com/spreadsheets/d/e/2PACX-1vQcF3FCmDq0mpyWy8tCjllh76g1V-6hSddorxKMImoKiVq7TDHXC3u14cugfn0sMRESX29zHvvJTRHn4/pubhtml
DEPARTMENT OF ELECTRICAL ENGINEERING	
SY	https://docs.google.com/spreadsheets/d/e/2PACX-1vRo4nVoq8S863x50wz1naosul8rhZEcMy5GjQVCA5ypzOnbEjvpw-epWUOmM9dn1_Om_jsD9azar7rc/pubhtml
TY	https://docs.google.com/spreadsheets/d/e/2PACX-1vST9C_RIVjhYK_9nfEGHO2i6033-axs-McW2rvVPDF8vn-cNDiSAekGgqKhPFES6owU1_RUZj3mAg0V/pubhtml
DEPARTMENT OF MASTER & BUSINESS ADMINISTRATION	
https://drive.google.com/file/d/1GcibfWt_b8z2yOuPLDhMbP9qnuUY5DFo/view?usp=sharing	

SMART BOARD

PURCHASE ORDER



Shri Vithal Education & Research Institute's

COLLEGE OF ENGINEERING, PANDHARPUR

P.B.No 54, Gopalpur - Ranjani Road, Gopalpur, Pandharpur - 413304, District: Solapur (Maharashtra)
 Tel.: 7755990201 Toll Free No.: 1800-3000-4131 e-mail.: coe@sveri.ac.in Website.: www.sveri.ac.in
 (Approved by A.I.C.T.E., New Delhi and Affiliated to Solapur University, Solapur)
 Accredited by The Indian Institution of Engineers (India), Kolkata and TCS, Pune.
 NAAC Accredited Institute, NBA Accredited All UG Programmes,
 ISO 9001:2008 Certified Institute.



Ref.: COEPR / 2018-2019 / 1394

Date: 27/10/2018

To,
 Vaibhav Computers,
 S.S.Front Road, Vijayapur,
 Pin 586101.

Subject: - Purchase order for Computer Systems and Peripheral Devices.
 Ref.: - Your Tender Quotation dated 06/09/2018 and further negotiation.

Dear Sir,
 With the above reference, we are pleased to place the Purchase Order for Computer Systems and Peripheral Devices
 for our College as per the details given below.

Sr. No.	Particulars	Qty	Rate per unit (Rs.)	GST %	Amount in including GST(Rs.)
01.	Make: HP i5 8500 Processor Base Frequency 3.00 Hz, Max Turbo Frequency 4.10 Hz Cache 9 MB Smart Cache BUS Speed 8 GT/s DMI3 TDP, 65W, RAM- 4GB, HDD-1TB, Inbuilt Graphics Card, Monitor- 21.5"	06	45000.00	18	318600.00
02.	Make: HP i5 8500 Processor Base Frequency 3.00 Hz, Max Turbo Frequency 4.10 Hz Cache 9 MB Smart Cache BUS Speed 8 GT/s DMI3 TDP, 65W, RAM- 8GB, HDD-1TB, Inbuilt Graphics Card, Monitor- 21.5"	03	47000.00	18	166380.00
03.	Acer Veriton i3 DOS Intel H110, C13 7100, 4GB DDR3, 1 TB HDD, No ODD, USB K+M, Free DOS, 18.5" non TCO Monitor, MT Chassis/ 200 W SMPS.	50	27500.00	18	1622500.00
04.	HP Z240 Tower Workstation E3-1225v6 3.3 8M GT2 4C, 8GB DDR4-2400 nECC (1x8GB) RAM, 1TB, NVIDIA Quadro P1000 4GB, Win 10 Pro 64 for Workstations INDIA, DVDRW, 21.5" HP monitor.	05	110000.00	18	649000.00
05.	Epson LQ 1310 Dot matrix printer 24 pin.	10	13200.00	18	155760.00
06.	HP Laserjet 1020 plus Printer.	08	9800.00	18	92512.00
07.	Raptor IR 1WB Smart Board RYJC085C Ceramic Based Smart Board.	03	36000.00	18	127440.00
08.	Computer Sound: T Ball 2.0 Speaker.	08	850.00	18	8024.00
09.	Transcend DDR3 RAM 2GB.	55	800.00	18	51920.00
10.	EPSON EB S41 3300 lumen, versatile SVGA Projector HDMI Inputs, Wifi connectivity Long life.	15	25300.00	28	485760.00
				Total	3677896.00
In Words: Rs. Thirty Six Lacs Seventy Seven Thousand Eight Hundred Ninety Six Only.					

The order is subject to the following terms and conditions:

- Acceptance of material is subject to prior inspection and approval from our side.
- Prices are inclusive of all taxes, GST, duties, packing, forwarding, transportation up to our institute and installation.
- Delivery within one week.
- Warranty: a) Sr.No.01 to 04: Three years onsite comprehensive warranty from the date of satisfactory demonstration.
 b) Sr.No.05 to 09: One years onsite comprehensive warranty from the date of satisfactory demonstration.
 c) Sr.No.10: Two years onsite comprehensive warranty on projector and One year onsite comprehensive warranty on lamp from the date of satisfactory demonstration.
- Payment: 100% against successful installation & satisfactory demonstration.
- TDS, if any, will be deducted as per Government rules.

Yours faithfully,

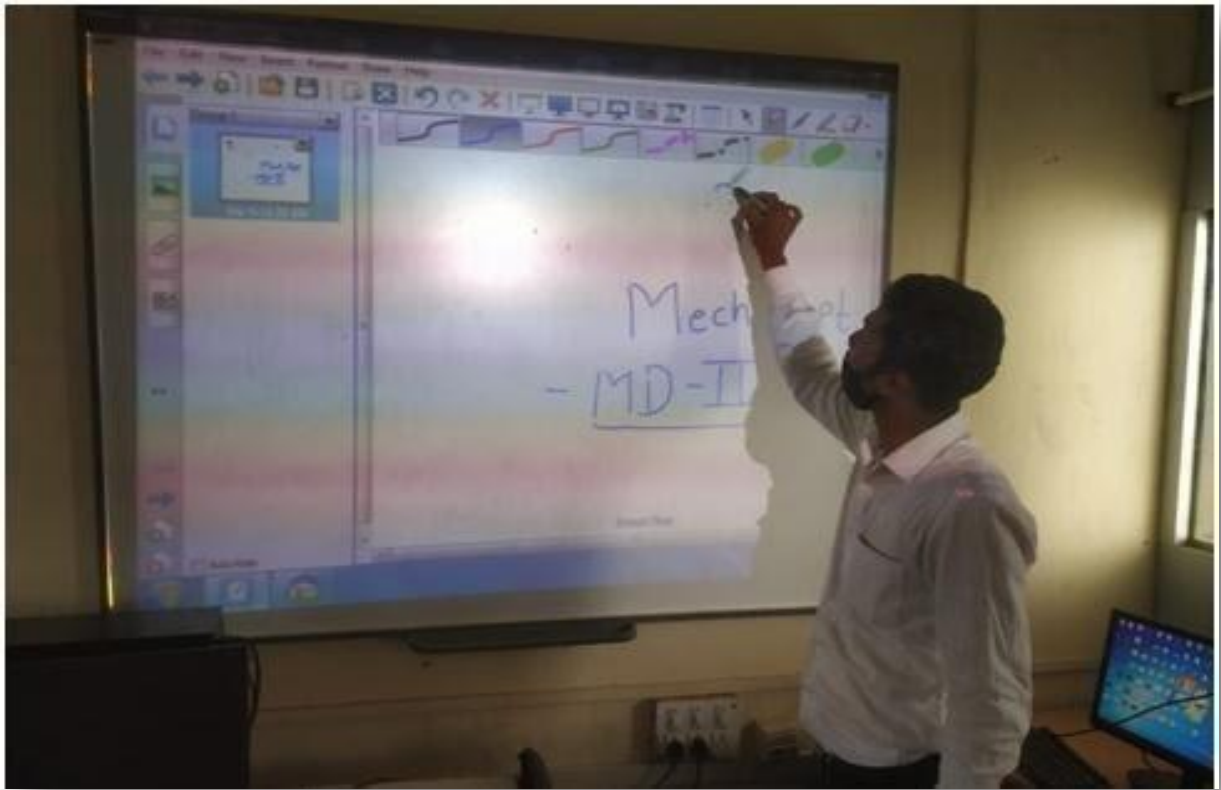
B. P. Ronge

(Dr. B. P. Ronge)
 PRINCIPAL



Received
B. P. Ronge





GOOGLE FORMS

SAMPLE 1

MCQ Test on Unit 1

Questions

Responses

87

Total points: 20

Section 1 of 3

MCQ Test on Unit 1



Information Systems in Global Business Today

After section 1 Continue to next section



Section 2 of 3

Untitled Section



Description (optional)

NAME *

Short answer text



Untitled Section



Description (optional)



1. Main task of Management information systems (MIS) is



- ☐ create and share documents that support day-today o...
- ☐ process business transactions (e.g., time cards, paym...
- ☐ capture and reproduce the knowledge of an expert pro...
- ☐ use the transaction data to produce information for m...

2. If a university sets up a web-based information system that faculty could access to record student grades and to advise students, that would be an example of a/an



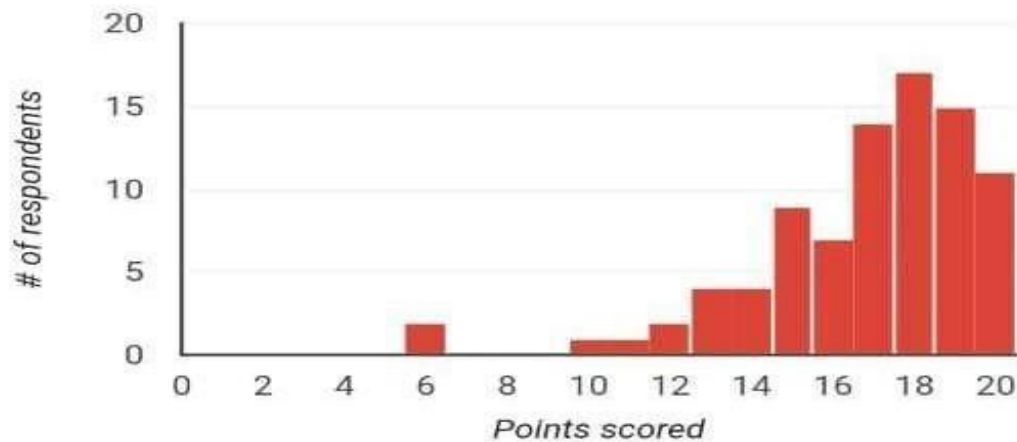
Average
16.79 / 20 points

Median
17 / 20 points

Range
6 - 20 points



Total points distribution



Frequently missed questions



Question

Correct responses

7. Which type of information system does not actually support decision making? 42 / 87

13. The MIS helps the ____ management in goal setting, strategic planning and evolving the business plan and their implementations. 27 / 87

SAMPLE 2

2/11/2021

Digital Design and HDL UT-3 - Google Forms



Digital Design and HDL UT-3

Questions Responses 56

Total points: 20

Section 1 of 2

Digital Design and HDL UT-3



Form description

Email address *

Valid email address

This form is collecting email addresses. [Change settings](#)

By Name of Student



Short answer text

Roll No *

Short answer text

Division *



B

After section 1 Go to section 2 (Multiple choice questions (1 mark each))



Section 2 of 2

Multiple choice questions (1 mark each)



Description (optional)

1. Which hardware description language is more popular in Europe? *

- ☐ a) VHDL
- ☐ b) System log
- ☐ c) Verilog
- ☐ d) C

2. In verilog `h1234 is a

- ☐ A. 16 bit hexadecimal number
- ☐ B. 32 bit hexadecimal number
- ☐ C. 4 bit hexadecimal number
- ☐ D. It is invalid notation

3. Which logic level is not supported by Verilog?

- ☐ A. U
- ☐ B. X
- ☐ C. Z



4. If $A = 4'b011$ and $B = 4'b'0011$, then the result of $A \& B$ will be

- ☐ A. 6
- ☐ B. 9
- ☐ C. 27
- ☐ D. Invalid expression

5. Verilog is an IEEE standard

- ☐ A. IEEE 1346
- ☐ B. IEEE 1364
- ☐ C. IEEE 1394
- ☐ D. IEEE 1349

6. Externally, a output port must always connected to a

- ☐ A. net only
- ☐ B. a reg only
- ☐ C. either net or reg
- ☐ D. None of the above

7. Initial value of $a=1$ and $b=2$, then what will be final value if $a \leftarrow b$ always @ (posedge clock)



- ☐ A. $a=2, b=1$
- ☐ B. $a=1, b=2$
- ☐ C. Both a and b will have same value either 0 or 1
- ☐ D. None of the above

8. In a pure combinational circuit is it necessary to mention all the inputs in sensitivity list?

- ☐ A. No
- ☐ B. Yes
- ☐ C. It depends on the coding style
- ☐ D. None of these

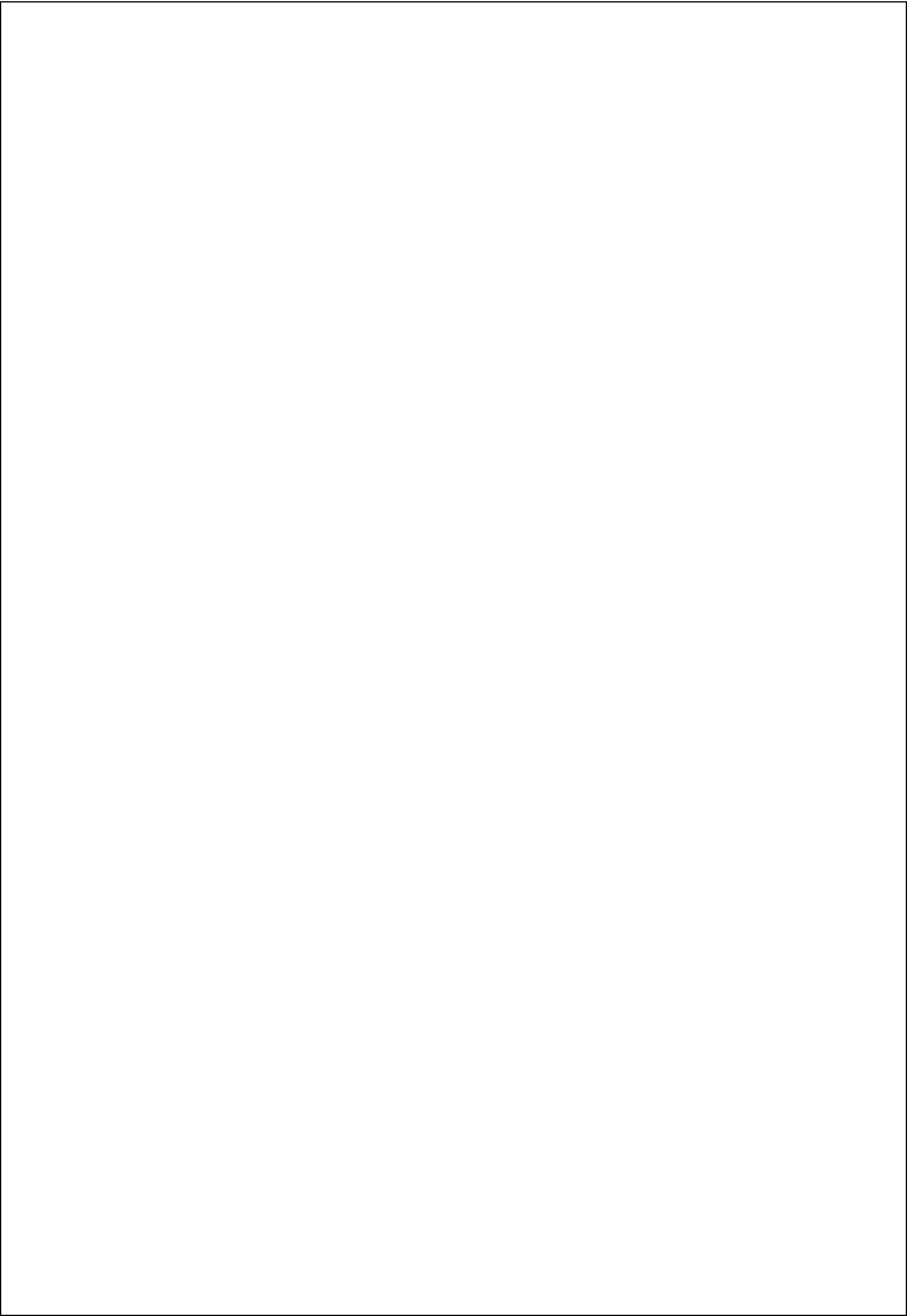
9. Which is not a correct method of specifying time scale in verilog?

- ☐ A. 1ns/1ps
- ☐ B. 10ns/1ps
- ☐ C. 100ns/100ps
- ☐ D. 100ns/110ps

10. The semantics of an "&" operator depends on the number of operands.

- ☐ A. True
- ☐ B. False





11. An "if" statement must always be inside of an "always" block

- ☐ A. True
- ☐ B. False

12. Which of the following is true about the always block?

- ☐ A. There can be exactly one always block in a design.
- ☐ B. There can be exactly one always block in a module.
- ☐ C. Execution of an always block occurs exactly once per simulation run.
- ☐ D. An always block may be used to generate a periodic signal.

13. Consider the following choices below. To comment Verilog Code, one may use: I. A "Double-slash" // for a single-line comment. II. Multiple "Double-slashes" (one per line) for a multiple-line comment.III. A "Block-comment" /* */ for a single-line comment.IV. A "Block-comment" /* */ for a multiple-line comment.

- ☐ A. I and II
- ☐ B. I and IV
- ☐ C. II and III
- ☐ D. I, II, and IV
- ☐ E. I, II, III and IV

14. Verilog is case sensitive



GOOGLE CLASSROOM

SAMPLE 1

The screenshot shows a Google Classroom interface for a course titled "DSY 2020-21-SEM-I". The page is viewed from the "Student work" tab. On the left, there is a sidebar with a "Return" button, a mail icon, and a "Sort by status" dropdown menu. Below this, a list of students is shown with checkboxes for "Turned in" status. The main area displays a grid of student submissions, each with a profile picture, name, and a preview of the work. The bottom of the screen shows a Windows taskbar with various application icons and system information.

Student	Status	Score	Work Type
Kiran Aigale	Turned in	100	Draft
yogita dongare	Turned in	100	Draft
rutuja gaikwad	Turned in	100	Draft
02_Reshma Gaygopal	Turned in	100	Draft
Rushi Ghogardare	Turned in	100	Draft
Chinmay Halsikar	Turned in	100	Draft

Submissions shown in the grid:

- Kiran Aigale: 2 attachments, Turned in
- yogita dongare: bitstuffing. c.pdf, Turned in
- rutuja gaikwad: BITSTUFF.C, Turned in
- 02_Reshma Gaygopal: bitstuffing. c.pdf, Turned in
- Rushi Ghogardare: bitstuffing.pdf, Turned in
- Chinmay Halsikar: 04chinmayhalsikarBIT..., Turned in
- PRADNYA INGALE: 2 attachments, Turned in
- anisha kambale: bit pgm.pdf, Turned in
- Aditya Keskar: [Image], Turned in
- neha koshti: [Image], Turned in
- Pranoti Koshti: [Image], Turned in
- Raviraj Kudal: [Image], Turned in

SAMPLE 2

[illegible]

VIRTUAL LAB

2015-16

Civil Engineering

Browser tabs: You are signed in as coe@cv74, Fwd: Forget Password - ybsurva, Virtual lab record - Google Drive, COEP::VLAB

Address bar: portal.coeplab.ac.in/vlab/auth/home?dept=7&lab=2

Apps: Google

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Gaikwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Civil Engineering

Lab Name: --Any--

Experiment Name: --A...

State Name: --Any--

From date: 01/07/2015

To date: 30/06/2016

Search

Activate Windows
Go to Settings to activate Windows.

Computer Science & Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Gaikwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Computer Engineering

Lab Name: --Any--

Experiment Name: --A...

State Name: --Any--

From date: 01/06/2015

To date: 30/06/2016

Search

Statistical ViewGraphical View

Total Simulator Time : 13 : 7 : 5

Activate Windows
Go to Settings to activate Windows.

Total Simulator Hit : 239

Electronics & Telecommunication Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Galkwad

Search ByGeneral

College NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area :Electronics and Communications

Lab Name :Any

Experiment Name :Any

State Name :Any

From date :01/07/2015

To date :30/06/2016

Search

Statistical ViewGraphical View

Total Simulator Time : 12 : 32 : 36Total Simulator Hit : 143

Show desktop

Mechanical Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Galkwad

Search ByGeneral

College NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area :Mechanical Engineering

Lab Name :Any

Experiment Name :Any

State Name :Maharashtra

From date :01/07/2015

To date :30/06/2016

Search

Statistical ViewGraphical View

Total Simulator Time : 7 : 5 : 8Total Simulator Hit : 229

Simulator Time : 0 : 18 : 39Simulator Hit : 10

2016-17

Civil Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

Home Records Welcome, Pankaj Gaikwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Civil Engineering

Lab Name: --Any--

Experiment Name: --A--

State Name: --Any--

From date: 01/07/2016

To date: 30/06/2017

Search

Statistical View Graphical View

Total Simulator Time : 0 : 4 : 36 Total Simulator Hit : 3

Simulator Time : 0 : 4 : 36 Simulator Hit : 3

User Id	Department	Lab	Experiment	Total Time (hour.min.sec)
ruturaj.murkar14@gmail.com	Civil Engineering	Strength of Materials Lab (IIITH)	Impact Test Experiment	0 : 1 : 42

Computer Science & Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

Home Records Welcome, Pankaj Gaikwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Computer Engineering

Lab Name: --Any--

Experiment Name: --A--

State Name: --Any--

From date: 01/07/2016

To date: 09/04/2017

Search

Statistical View Graphical View

Total Simulator Time : 9 : 45 : 35 Total Simulator Hit : 20

Electronics & Telecommunication Engineering



VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE



[Home](#) [Records](#)  Welcome, Pankaj Galkwad ▾

Search By: General ▾

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur ▾

Virtual Area: Electronics and Communications ▾

Lab Name: —Any— ▾

Experiment Name: —Any— ▾

State Name: —Any— ▾

From date: 01/07/2016

To date: 30/06/2017

Search

Statistical View Graphical View

Total Simulator Time : 1 : 13 : 26 Total Simulator Hit : 14

Mechanical Engineering



VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE



[Home](#) [Records](#)  Welcome, Pankaj Galkwad ▾

Search By: General ▾

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur ▾

Virtual Area: Mechanical Engineering ▾

Lab Name: —Any— ▾

Experiment Name: —Any— ▾

State Name: Maharashtra ▾

From date: 01/07/2016

To date: 30/06/2017

Search

Statistical View Graphical View

Total Simulator Time : 0 : 57 : 2 Total Simulator Hit : 22

Simulator Time : 0 : 41 : 18 Simulator Hit : 10

2017-18

Civil Engineering

You are signed in as coe@cv74 x Fwd: Forget Password - ybsurva x Virtual lab record - Google Drive x COEP :: VLAB x +

portal.coeplab.ac.in/vlab/auth/home?dept=7&lab=2

Apps Google

VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE

Home Records Welcome, Pankaj Gaikwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Civil Engineering

Lab Name: --Any--

Experiment Name: --A--

State Name: --Any--

From date: 01/07/2017

To date: 30/06/2018

Search

Statistical View Graphical View

Total Simulator Time : 0 : 36 : 40

Total Simulator Hit : 23

Computer Science & Engineering

VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE

Home Records Welcome, Pankaj Gaikwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Computer Engineering

Lab Name: --Any--

Experiment Name: --A--

State Name: --Any--

From date: 01/07/2017

To date: 09/06/2018

Search

Statistical View Graphical View

Total Simulator Time : 4 : 19 : 38

Total Simulator Hit : 100

Electronics & Telecommunication Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Galkwad

Search ByGeneral

College NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual AreaElectronics and Communications

Lab NameAny

Experiment NameAn...

State NameAny

From date01/07/2017

To date30/06/2018

Search

Statistical ViewGraphical View

Total Simulator Time : 3 : 6 : 41Total Simulator Hit : 133

Mechanical Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Galkwad

Search ByGeneral

College NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual AreaMechanical Engineering

Lab NameAny

Experiment NameA...

State NameMaharashtra

From date01/07/2017

To date30/06/2018

Search

Statistical ViewGraphical View

Total Simulator Time : 0 : 27 : 30Total Simulator Hit : 13

2018-19

Civil Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Gaikwad

Search ByGeneralCollege NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, SolapurVirtual AreaCivil EngineeringLab NameAnyExperiment NameAnyState NameAnyFrom date01/07/2018To date30/08/2019Search

Statistical ViewGraphical View

Total Simulator Time : 11 : 35 : 50Total Simulator Hit : 397

Simulator Time : 0 : 15 : 52Simulator Hit : 10

User Id	Department	Lab	Experiment	Total Time (hour.min.sec)
mhmaliptil@coe.sveri.ac.in	Civil Engineering	Surveying Lab	Profile Leveling using Auto Level	0 : 0 : 31
shashilal@coe.sveri.ac.in	Civil Engineering	Fluid Mechanics Lab	Flow of Fluids	0 : 0 : 53

Activate WGo to Settings

Computer Science & Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Gaikwad

Search ByGeneralCollege NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, SolapurVirtual AreaComputer EngineeringLab NameAnyExperiment NameAnyState NameAnyFrom date01/07/2018To date09/06/2019Search

Statistical ViewGraphical View

Total Simulator Time : 74 : 49 : 32Total Simulator Hit : 1892

Electronics & Telecommunication Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Galkwad

Search ByGeneral

College NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area :Electronics and Communications

Lab Name :Any

Experiment Name :Any

State NameAny

From date :01/07/2018

To date :30/06/2019

Search

Statistical ViewGraphical View

Total Simulator Time : 63 : 9 : 0Total Simulator Hit : 1036

Mechanical Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Galkwad

Search ByGeneral

College NameNC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area :Mechanical Engineering

Lab Name :Any

Experiment Name :Any

State NameMaharashtra

From date :01/07/2018

To date :30/06/2019

Search

Statistical ViewGraphical View

Total Simulator Time : 40 : 40 : 44Total Simulator Hit : 520

Simulator Time : 0 : 13 : 36Simulator Hit : 10

2019-20

Civil Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Gaikwad ▾

Search By:

College Name:

Virtual Area:

Lab Name:

Experiment Name:

State Name:

From date:

To date:

Search

Statistical ViewGraphical View

Total Simulator Time : 27 : 12 : 18Total Simulator Hit : 444

Computer Science & Engineering

**VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE**

HomeRecordsWelcome, Pankaj Gaikwad ▾

Search By:

College Name:

Virtual Area:

Lab Name:

Experiment Name:

State Name:

From date:

To date:

Search

Statistical ViewGraphical View

Total Simulator Time : 42 : 27 : 41Total Simulator Hit : 1319

Electronics & Telecommunication Engineering



VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE



HomeRecordsWelcome, Pankaj Galkwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Electronics and Communications

Lab Name: Any

Experiment Name: Any

State Name: Any

From date: 01/07/2019

To date: 30/06/2020

Search

Statistical ViewGraphical View

Total Simulator Time : 53 : 55 : 23Total Simulator Hit : 711

Mechanical Engineering



VIRTUAL LABS @ COLLEGE OF ENGINEERING PUNE



HomeRecordsWelcome, Pankaj Galkwad

Search By: General

College Name: NC 15 Shri Vitthal Education & Research Institute, Pandharpur, Solapur

Virtual Area: Mechanical Engineering

Lab Name: Any

Experiment Name: Any

State Name: Maharashtra

From date: 01/07/2019

To date: 30/06/2020

Search

Statistical ViewGraphical View

Total Simulator Time : 20 : 15 : 16Total Simulator Hit : 480

SVNET

SVERI's COEP

[dae.sveri.edu/svnet/](#)

30%

[Most Visited](#)
[Getting Started](#)
[Web Slice Gallery](#)
[Suggested Sites](#)



SVERI's - Virtual Network

Knowledge enrichment programme using information communication technology (ICT) & virtual lab

Objectives

- To enthuse students for self-learning.
- To provide remote access to best contents available virtually.
- To provide complete learning management system.
- To extend to which laboratory activities helps students understand key concepts.



Objectives

- To be recognized amongst best institute in India for excellence in technical education.
- To impart value based technical education by inculcating personal touch and respect in relationship amongst stakeholders.

 CSE
 MECH
 ENTC
 FE
 MBA
 IT
 Civil

A Project of
SVERI's COE, Pandharpur

Designed by
Udayraj S. Nanjunde

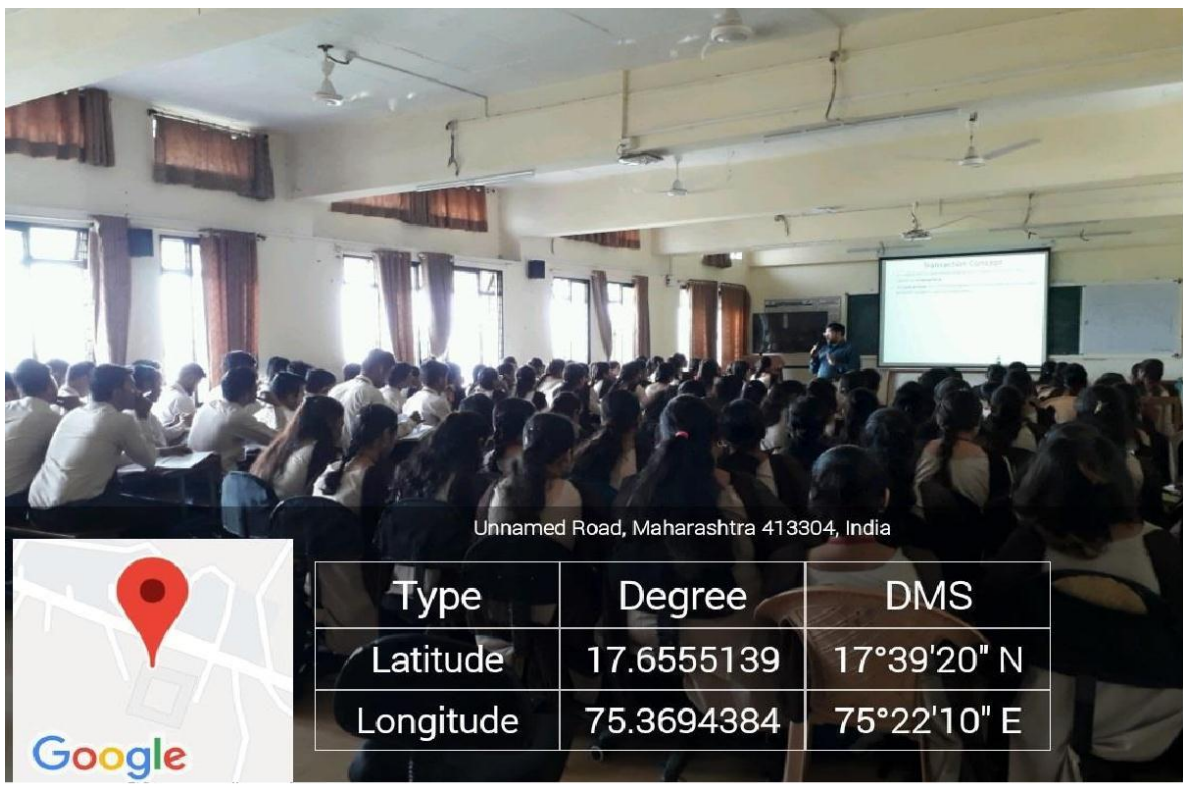


9:10 AM
10/6/2019

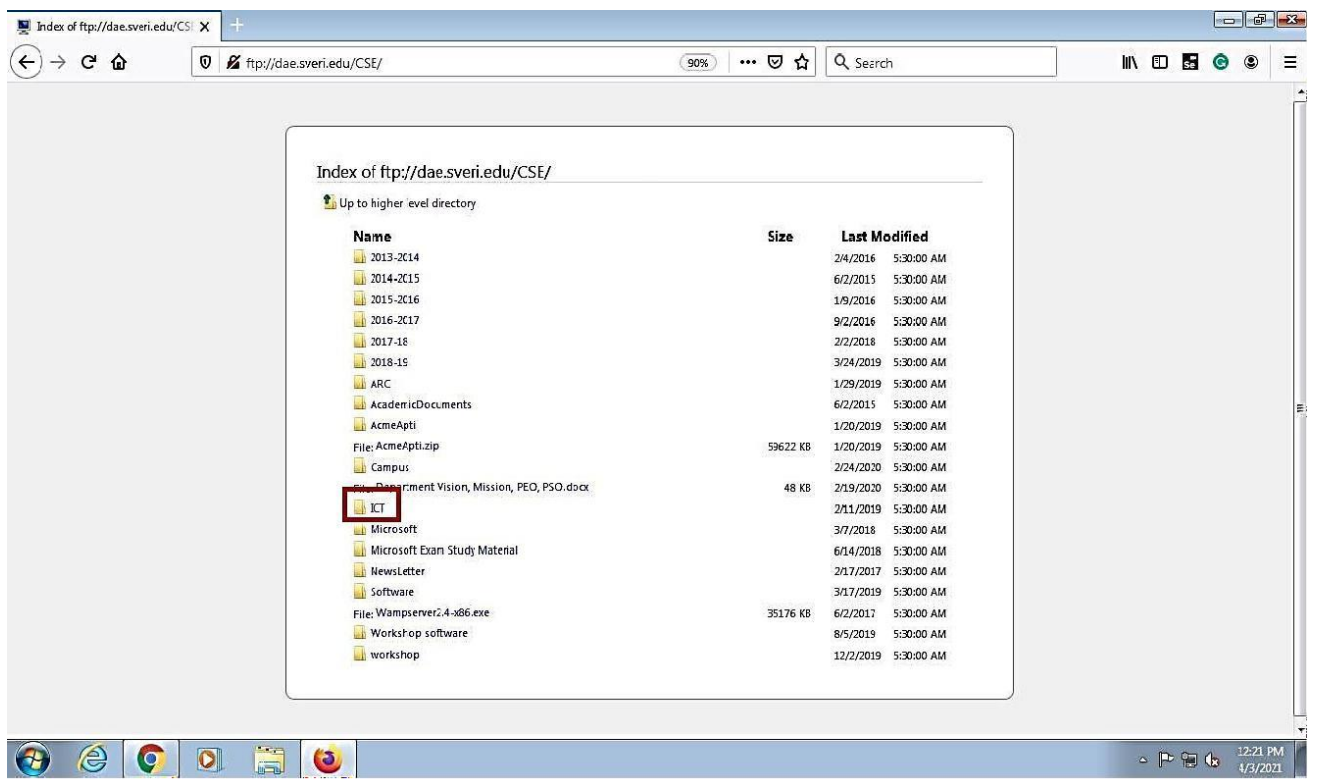
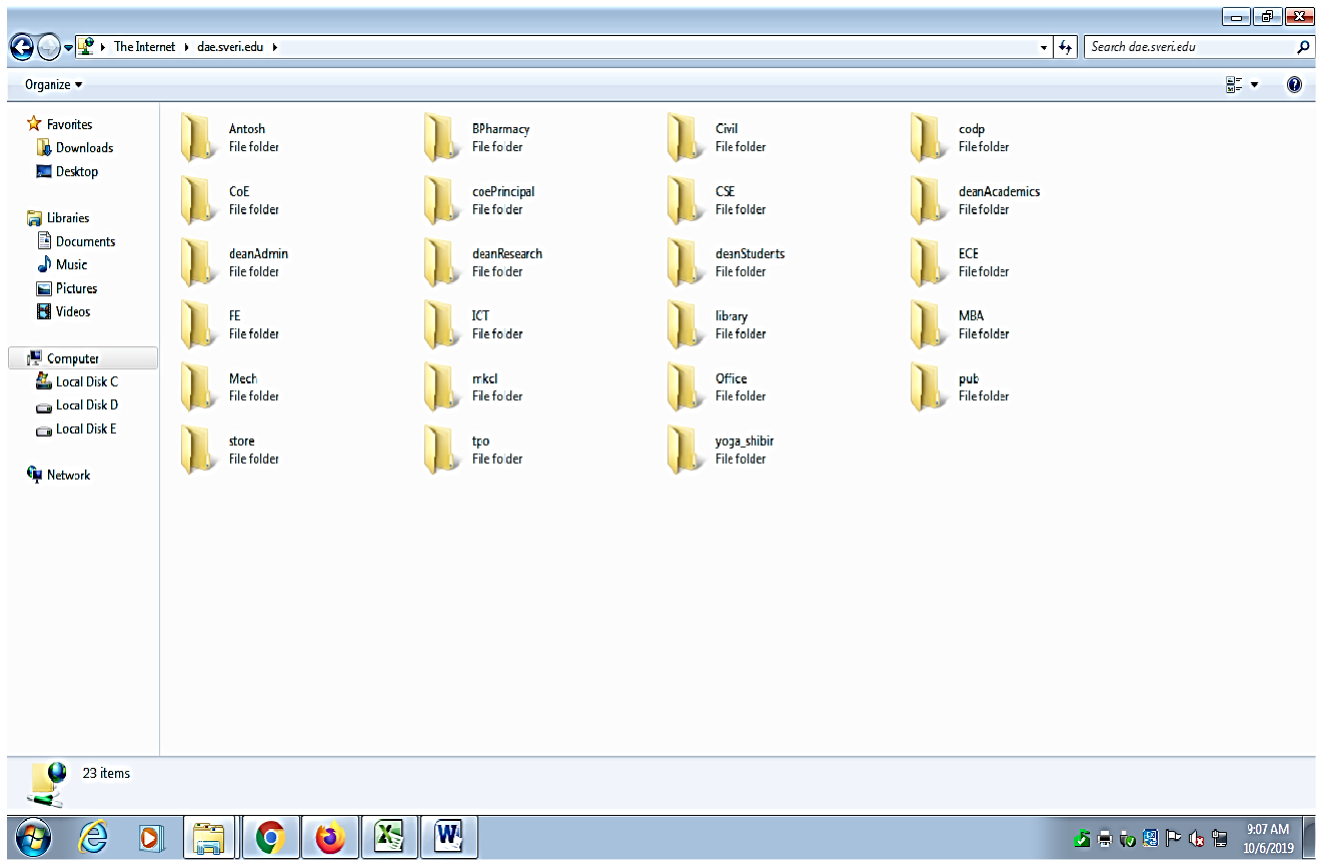
 Search Institute's ENGINEERING, PANDHARPUR P.B. No. 54, Gopalpur -Ranjani Road, Gopalpur, Pandharpur- 413 304, District: Solapur (Maharashtra) Tel.: 02186-216063, 9503103757, Toll Free No.: 1800-3000-4131, E-mail: coe@sveri.ac.in, Web: www.sveri.ac.in (Approved by A.I.C.T.E., New Delhi and Affiliated to Solapur University, Solapur NBA Accredited all eligible UG Programmes, NAAC Accredited Institute, Accredited by The Institution of Engineers (India), Kolkata and TCS, Pune. ISO 9001-2015 Certified Institute	
Second Year (Electronics and Telecommunication Engineering) A.Y. 2018-19	
First Semester - Electronics Circuit Analysis and Design-I [A] / [B] - Circuits and Network [A] / [B] - Digital Techniques [A] / [B] - Analog Communication [A] / [B] - Electronics Software Lab-I [A] / [B] - Engineering Mathematics-III [A] / [B]	Second Semester - Linear Integrated Circuits [A] / [B] - Electronic Circuit Analysis and Design-II [A] / [B] - Data Structures [A] / [B] - Control Systems [A] / [B] - Signals and Systems [A] / [B] - Electronics Software Lab-I [A] / [B]
Third Year (Electronics and Telecommunication Engineering) A.Y. 2018-19	
First Semester - Electro Magnetic Engineering & Radiating System [A] / [B] - Principles of Digital Communication [A] / [B] - Software Engineering & Project Management System [A] / [B] - Digital Signal Processing [A] [Lab] / [B] - Microprocessors [A] / [B] - Electronic Software Lab-II [A] / [B]	Second Semester - Radar & Microwave Engineering [A] / [B] - Microcontrollers & Applications [A] / [B] - Electronics Applications & System Design [A] / [B] - Optical Communication [A] / [B] - Mobile Communication [A] / [B] - Mini Project (Hardware) [A] / [B]
Final Year (Electronics and Telecommunication Engineering) A.Y. 2018-19	
First Semester - Computer Communication Network [A] / [B] - VLSI Design [A] / [B] - Satellite communication [A] / [B] - Coding Theory [A] / [B] - Elective – I (Image Processing) [A] / [B]	Second Semester - Broadband Communication [A] / [B] - Multimedia Communication Techniques [A] / [B] - Embedded Systems [A] / [B] - Elective – II (Pattern Rec.) [A] / [B] - Wireless Sensor Network [A] / [B] - Elective – II (Internet of Things) [A] / [B]

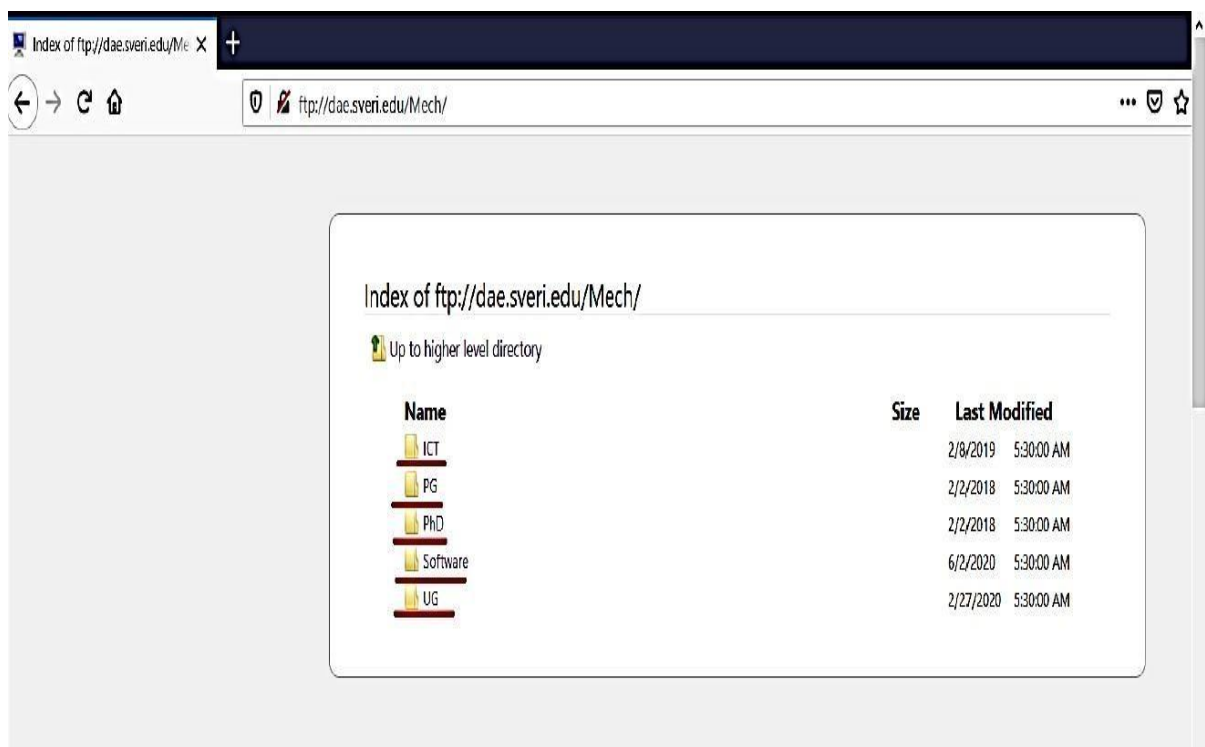
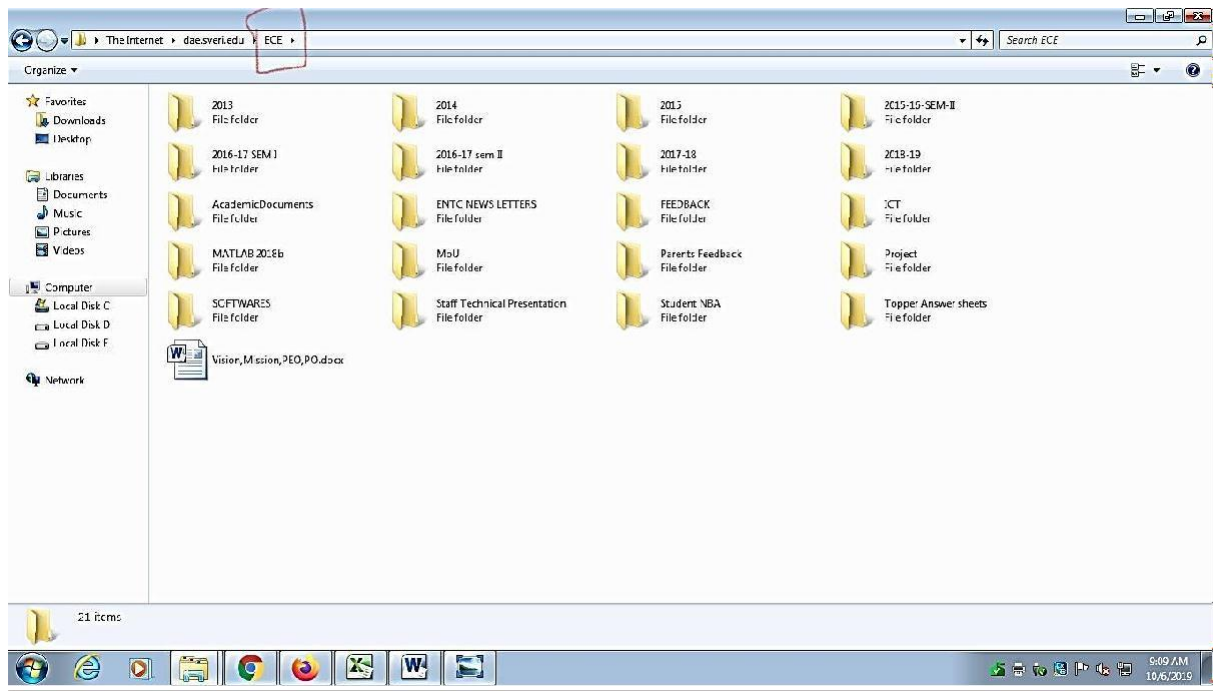
Second Year	
First Semester	Second Semester
• Analysis of Mechanical Elements • Applied Thermodynamics • Manufacturing Processes • Engineering Mathematics-III • Machine Drawing • CPP	• CAMD • EET • Fluid Mechanics • Theory of Machines • Numerical Methods • MTP
Third Year	
First Semester	Second Semester
• MMM • TOM-II • Metallurgy • Machine Design-I • Fluid Machinery & Fluid Power • ACP-I • Self Learning	• CAD CAM CAE • Tool Engineering • IC Engine • Heat and Mass Transfer • MD-II • ESA
Final Year	
First Semester	Second Semester
• Automatic Control Engineering • Operations Research • Refrigeration & Air Conditioning • Automobile Engineering • Entrepreneurship Development	• IQM • Industrial Engineering • Plastic Engineering • Production and Operation Management

LCD PROJECTOR



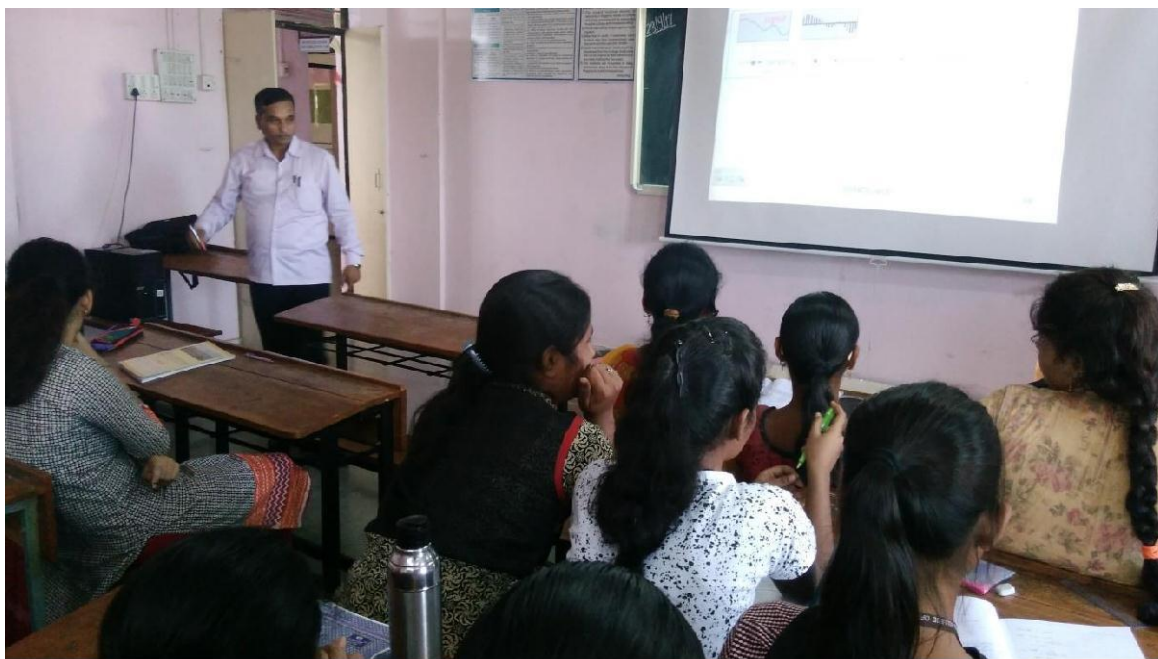
FTP





PPT

	PPT LINKS
	https://drive.google.com/file/d/1RSenJjQ96pY8pc0KARa0SbBp1FsW8-L4/view?usp=sharing
	https://drive.google.com/file/d/15rX5BKxj7j2VmrsR_6GU8WLovmvKPR2S/view?usp=sharing
	https://drive.google.com/file/d/14YcJJN81o0VsvEpMOCb7b_tOqoHO7n0X/view?usp=sharing
	https://drive.google.com/a/coe.sveri.ac.in/file/d/10bCAT9k_BzB_Jto_NBNwW1VgHW0g4kM8/view?usp=drivesd k
	https://drive.google.com/a/coe.sveri.ac.in/file/d/10bCAT9k_BzB_Jto_NBNwW1VgHW0g4kM8/view?usp=drivesd k



ERP-based CO-PO Attainment

Outcome Based Education (OBE) System is implemented through RWork (ERP) software and is explained using a course Electro Magnetic Engg. & Radiating System (ET311-18) of Third Year under Program UG in Electronics and Telecommunication Engineering as follows

Step 1: PEOs, POs and PSOs are defined for each Academic Year

PEO, PO And PSO

Create/View/Update PEO, PO & PSO

Note : * Indicates Mandatory Fields

Academic Year* 2018-19 Program* UNDER GRADUATE IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING(1ET1)

Program Educational Objectives (PEOs) Information [Download Report](#)

Sr. No.	PEO No.	PEO Statements	Approved In	Action
1	PEO1	FUNCTION SUCCESSFULLY IN A PROFESSIONAL ENVIRONMENT USING THE TECHNICAL EXPERTISE IN THEIR CAREER FOR CONTEMPORARY PROBLEM SOLVING IN THE FIELD OF ELECTRONICS, COMMUNICATION ENGINEERING AND ALLIED BRANCHES.	BOARD OF GOVERNANCE : 25-03-2018	Update
2	PEO2	RESPOND TO THE GROWING AND CHANGING NEEDS OF SOCIETY THROUGH LIFELONG LEARNING TO EVOLVE INNOVATIVE SOLUTIONS.	BOARD OF GOVERNANCE : 25-03-2018	Update
3	PEO3	DEMONSTRATE LEADERSHIP, COMMITMENT AND MAINTAIN ETHICS IN THEIR CAREER.	BOARD OF GOVERNANCE : 25-03-2018	Update
4	PEO4	DEMONSTRATE EFFECTIVE COMMUNICATION SKILLS AND THE ABILITY TO WORK EFFICIENTLY AT INDIVIDUAL LEVEL AND AS PART OF A TEAM.	BOARD OF GOVERNANCE : 25-03-2018	Update

Program Outcomes (POs) Information [Download Report](#)

Sr. No.	PO No.	PO Statements	Approved In	Action
1	PO1	APPLY THE KNOWLEDGE OF MATHEMATICS, SCIENCE, ENGINEERING FUNDAMENTALS, AND AN ENGINEERING SPECIALIZATION TO THE SOLUTION OF COMPLEX ENGINEERING PROBLEMS.	BOARD OF GOVERNANCE : 25-03-2018	Update
2	PO2	IDENTIFY, FORMULATE, REVIEW RESEARCH LITERATURE, AND ANALYZE COMPLEX ENGINEERING PROBLEMS REACHING SUBSTANTIATED CONCLUSIONS USING FIRST PRINCIPLES OF MATHEMATICS, NATURAL SCIENCES, AND ENGINEERING SCIENCES.	BOARD OF GOVERNANCE : 25-03-2018	Update
3	PO3	DESIGN SOLUTIONS FOR COMPLEX ENGINEERING PROBLEMS AND DESIGN SYSTEM COMPONENTS OR PROCESSES THAT MEET THE SPECIFIED NEEDS WITH APPROPRIATE CONSIDERATION FOR THE PUBLIC HEALTH AND SAFETY, AND THE CULTURAL, SOCIETAL, AND ENVIRONMENTAL CONSIDERATIONS.	BOARD OF GOVERNANCE : 25-03-2018	Update
4	PO4	USE RESEARCH-BASED KNOWLEDGE AND RESEARCH METHODS INCLUDING DESIGN OF EXPERIMENTS, ANALYSIS AND INTERPRETATION OF DATA, AND SYNTHESIS OF THE INFORMATION TO PROVIDE VALID CONCLUSIONS.	BOARD OF GOVERNANCE : 25-03-2018	Update
5	PO5	CREATE, SELECT, AND APPLY APPROPRIATE TECHNIQUES, RESOURCES, AND MODERN ENGINEERING AND IT TOOLS INCLUDING PREDICTION AND MODELING TO COMPLEX ENGINEERING ACTIVITIES WITH AN UNDERSTANDING OF THE LIMITATIONS.	BOARD OF GOVERNANCE : 25-03-2018	Update
6	PO6	APPLY REASONING INFORMED BY THE CONTEXTUAL KNOWLEDGE TO ASSESS SOCIETAL, HEALTH, SAFETY, LEGAL AND CULTURAL ISSUES AND THE CONSEQUENT RESPONSIBILITIES RELEVANT TO THE PROFESSIONAL ENGINEERING PRACTICE.	BOARD OF GOVERNANCE : 25-03-2018	Update
7	PO7	UNDERSTAND THE IMPACT OF THE PROFESSIONAL ENGINEERING SOLUTIONS IN SOCIETAL AND ENVIRONMENTAL CONTEXTS, AND DEMONSTRATE THE KNOWLEDGE OF, AND NEED FOR SUSTAINABLE DEVELOPMENT.	BOARD OF GOVERNANCE : 25-03-2018	Update
8	PO8	APPLY ETHICAL PRINCIPLES AND COMMIT TO PROFESSIONAL ETHICS AND RESPONSIBILITIES AND NORMS OF THE ENGINEERING PRACTICE.	BOARD OF GOVERNANCE : 25-03-2018	Update
9	PO9	FUNCTION EFFECTIVELY AS AN INDIVIDUAL, AND AS A MEMBER OR LEADER IN DIVERSE TEAMS, AND IN MULTIDISCIPLINARY SETTINGS.	BOARD OF GOVERNANCE : 25-03-2018	Update
10	PO10	COMMUNICATE EFFECTIVELY ON COMPLEX ENGINEERING ACTIVITIES WITH THE ENGINEERING COMMUNITY AND WITH SOCIETY AT LARGE, SUCH AS, BEING ABLE TO COMPREHEND AND WRITE EFFECTIVE REPORTS AND DESIGN DOCUMENTATION, MAKE EFFECTIVE PRESENTATIONS, AND GIVE AND RECEIVE CLEAR INSTRUCTIONS.	BOARD OF GOVERNANCE : 25-03-2018	Update
11	PO11	DEMONSTRATE KNOWLEDGE AND UNDERSTANDING OF THE ENGINEERING AND MANAGEMENT PRINCIPLES AND APPLY THESE TO ONE'S OWN WORK, AS A MEMBER AND LEADER IN A TEAM, TO MANAGE PROJECTS AND IN MULTIDISCIPLINARY ENVIRONMENTS.	BOARD OF GOVERNANCE : 25-03-2018	Update
12	PO12	RECOGNIZE THE NEED FOR, AND HAVE THE PREPARATION AND ABILITY TO ENGAGE IN INDEPENDENT AND LIFE-LONG LEARNING IN THE BROADEST CONTEXT OF TECHNOLOGICAL CHANGE.	BOARD OF GOVERNANCE : 25-03-2018	Update

Program Specific Outcomes (PSOs) Information [Download Report](#)

RWork

svet.rwork.tech/coe/programme_management/manage_ppp/index

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Reading list

Sr. No.	PO	Statement	Board of Governance	Action
5	PO5	CREATE, SELECT, AND APPLY APPROPRIATE TECHNIQUES, RESOURCES, AND MODERN ENGINEERING AND IT TOOLS INCLUDING PREDICTION AND MODELING TO COMPLEX ENGINEERING ACTIVITIES WITH AN UNDERSTANDING OF THE LIMITATIONS.	BOARD OF GOVERNANCE : 25-03-2018	Update
6	PO6	APPLY REASONING INFORMED BY THE CONTEXTUAL KNOWLEDGE TO ASSESS SOCIETAL, HEALTH, SAFETY, LEGAL AND CULTURAL ISSUES AND THE CONSEQUENT RESPONSIBILITIES RELEVANT TO THE PROFESSIONAL ENGINEERING PRACTICE.	BOARD OF GOVERNANCE : 25-03-2018	Update
7	PO7	UNDERSTAND THE IMPACT OF THE PROFESSIONAL ENGINEERING SOLUTIONS IN SOCIETAL AND ENVIRONMENTAL CONTEXTS, AND DEMONSTRATE THE KNOWLEDGE OF, AND NEED FOR SUSTAINABLE DEVELOPMENT.	BOARD OF GOVERNANCE : 25-03-2018	Update
8	PO8	APPLY ETHICAL PRINCIPLES AND COMMIT TO PROFESSIONAL ETHICS AND RESPONSIBILITIES AND NORMS OF THE ENGINEERING PRACTICE.	BOARD OF GOVERNANCE : 25-03-2018	Update
9	PO9	FUNCTION EFFECTIVELY AS AN INDIVIDUAL, AND AS A MEMBER OR LEADER IN DIVERSE TEAMS, AND IN MULTIDISCIPLINARY SETTINGS.	BOARD OF GOVERNANCE : 25-03-2018	Update
10	PO10	COMMUNICATE EFFECTIVELY ON COMPLEX ENGINEERING ACTIVITIES WITH THE ENGINEERING COMMUNITY AND WITH SOCIETY AT LARGE, SUCH AS, BEING ABLE TO COMPREHEND AND WRITE EFFECTIVE REPORTS AND DESIGN DOCUMENTATION, MAKE EFFECTIVE PRESENTATIONS, AND GIVE AND RECEIVE CLEAR INSTRUCTIONS.	BOARD OF GOVERNANCE : 25-03-2018	Update
11	PO11	DEMONSTRATE KNOWLEDGE AND UNDERSTANDING OF THE ENGINEERING AND MANAGEMENT PRINCIPLES AND APPLY THESE TO ONE'S OWN WORK, AS A MEMBER AND LEADER IN A TEAM, TO MANAGE PROJECTS AND IN MULTIDISCIPLINARY ENVIRONMENTS.	BOARD OF GOVERNANCE : 25-03-2018	Update
12	PO12	RECOGNIZE THE NEED FOR, AND HAVE THE PREPARATION AND ABILITY TO ENGAGE IN INDEPENDENT AND LIFE-LONG LEARNING IN THE BROADEST CONTEXT OF TECHNOLOGICAL CHANGE.	BOARD OF GOVERNANCE : 25-03-2018	Update

Program Specific Outcomes (PSOs) Information

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Sr. No.	PSO No.	PSO Statements	Approved In	Action
1	PSO1	DESIGN, DEVELOP AND DEMONSTRATE EXPERIMENTS, ANALYZE & INTERPRET DATA IN THE AREAS OF ANALOG & DIGITAL DESIGN, COMMUNICATION SYSTEMS AND ALLIED BRANCHES.	BOARD OF GOVERNANCE : 25-03-2018	Update
2	PSO2	APPLY KNOWLEDGE OF ELECTRONICS & TELECOMMUNICATION ENGINEERING TO MEET THE DESIRED NEEDS WITHIN REALISTIC CONSTRAINTS VIZ. ECONOMIC, ENVIRONMENTAL, SOCIAL & ETHICAL.	BOARD OF GOVERNANCE : 25-03-2018	Update
3	PSO3	USE THE TECHNIQUES, SKILLS, AND MODERN ENGINEERING TOOLS NECESSARY FOR ELECTRONICS & TELECOMMUNICATION ENGINEERING.	BOARD OF GOVERNANCE : 25-03-2018	Update

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RWork

Step 2: COs are defined for a course

RWork

svet.rwork.tech/coe/course_management/assign_course/index

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Reading list

SVERI'S COLLEGE OF ENGINEERING, PANDHARPUR Admin

My Dashboard Admissions Employee Record Student Center Employee Profile Academics

Dashboard TimeTable & Attendance Vision & Mission Program Management Course Evaluation Tools Course Management

Manage Courses Manage Divisions & Batches Class Coordinators Course - Faculty Linking Course Details Course - PO Mapping Index CO Targets Student Enrollment Evaluation

First Year Academics Examination Document Issuance Library Management

Type here to search

Course Details

View/Update Course Details

Course Information Syllabus Course CO Information Course Tool Information

Academic Year* 2018-19 Program* UNDER GRADUATE IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (UGET)

Class* THIRD YEAR Semester* SEMESTER I

Division* A Course* ELECTRO MAGNETIC ENGG. & RADIATING SYSTEM (ET311-18)

Sr. No.	CO Code	CO Statements	Bloom's Level	Action
1	ET311-18.1	UTILIZE THE MATHEMATICAL FUNDAMENTALS WHICH ARE REQUIRED TO DERIVE BASICS OF ELECTROMAGNETIC FIELD.	BL3 APPLY	Edit
2	ET311-18.2	INTERPRET AND FORMULATE THE CONCEPTS OF ELECTROSTATIC FIELD.	BL4 ANALYZE	Edit
3	ET311-18.3	SOLVE NUMERICAL ON ELECTROSTATIC FIELD.	BL3 APPLY	Edit
4	ET311-18.4	EXAMINE VARIOUS LAWS OF MAGNETO STATIC FIELD.	BL4 ANALYZE	Edit
5	ET311-18.5	SUMMARIES MAXWELL'S EQUATION TO INTERPRET WAVE PROPAGATION.	BL5 EVALUATE	Edit
6	ET311-18.6	SELECT, DESIGN AND SIMULATE AN ANTENNA FOR SPECIFIC APPLICATION.	BL6 CREATE	Edit

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Type here to search

Step 3: CO-PO mapping index is prepared

Course - PO Mapping Index

CO-PO Matrix

Academic Year 2018-19

Degree Level UNDER GRADUATE

Class THIRD YEAR

Division A

Program UNDER GRADUATE IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Department ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Semester SEMESTER I

Course ELECTRO MAGNETIC ENGG. & RADIATING SYSTEM (ET311-18)

Note: * Indicates Mandatory Fields

Level of Co-relation

No Co-relation: 0 Low Co-relation: 1 Medium Co-relation: 2 High Co-relation: 3

Sr. No.	CO Code	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PS02	PS03
1	ET311-18.1	3	3	3	3	1	2	1	1	2	1	2	3	3	3	3
2	ET311-18.2	2	2	3	3	2	1	1	NA	1	1	1	1	3	3	3
3	ET311-18.3	3	2	2	3	3	2	2	1	1	1	1	2	3	3	3
4	ET311-18.4	2	2	1	2	1	1	1	NA	2	1	2	1	3	3	3
5	ET311-18.5	1	1	1	2	1	1	1	NA	1	1	1	2	3	3	3
6	ET311-18.6	1	3	3	3	3	2	1	1	2	1	3	3	3	3	3

Course PO Matrix

Sr. No.	Course Code	Course Name	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PS02	PS03
1	ET311-18	ELECTRO MAGNETIC ENGG. & RADIATING SYSTEM	2	2	2	3	2	2	1	1	2	1	2	2	3	3	3

Update Download Report Back

Step 4: Tools (Internal and External) are defined for CO Attainment

Course Details

View/Update Course Details

Course Information Syllabus Course CO Information Course Tool Information

Academic Year* 2018-19

Program* UNDER GRADUATE IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (1ET1)

Class* THIRD YEAR

Semester* SEMESTER I

Division* A

Sr. No.	Course Code	Course Name	Assigned Tools	Action
1	ET311-18	ELECTRO MAGNETIC ENGG. & RADIATING SYSTEM	ESE, ISE-1, OBT-1, THT-1, ISE-2, OBT-2, THT-2, ISE-3, OBT-3, THT-3, ISE, UT-1, UT-2, UT-3, ASSIGNMENT, ICA, FISE-1, FISE-2, FISE-3, TUTORIAL, PPPE, LAB BOOK, LAB TEST 1, ICA-D, LAB TEST 2, LAB TEST	Unfreeze
2	ET312-18	PRINCIPLES OF DIGITAL COMMUNICATION	ESE, ISE-1, OBT-1, THT-1, ISE-2, OBT-2, THT-2, ISE-3, OBT-3, THT-3, ISE, UT-1, UT-2, UT-3, ASSIGNMENT, PPPE, LAB BOOK, LAB TEST 1, POE, ICA, FISE-1, FISE-2, FISE-3, ICA-D, LAB TEST 2, LAB TEST	Unfreeze
3	ET313-18	SOFTWARE ENGINEERING & PROJECT MANAGEMENT SYSTEM	ESE, ISE-1, OBT-1, THT-1, ISE-2, OBT-2, THT-2, ISE-3, OBT-3, THT-3, ISE, UT-1, UT-2, UT-3, ASSIGNMENT, PPPE	Unfreeze
4	ET314-18	DIGITAL SIGNAL PROCESSING	ESE, ISE-1, OBT-1, THT-1, ISE-2, OBT-2, THT-2, ISE-3, OBT-3, THT-3, ISE, UT-1, UT-2, UT-3, ASSIGNMENT, PPPE, LAB BOOK, LAB TEST 1, POE, ICA-D, ICA, LAB TEST 2, LAB TEST	Unfreeze
5	ET315-18	MICROCONTROLLER - I (8051)	ESE, ISE-1, OBT-1, THT-1, ISE-2, OBT-2, THT-2, ISE-3, OBT-3, THT-3, ISE, UT-1, UT-2, UT-3, ASSIGNMENT, PPPE, LAB BOOK, LAB TEST 1, POE, ICA-D, ICA, LAB TEST 2, LAB TEST	Unfreeze
6	ET316-18	ELECTRONIC SOFTWARE LAB-III	TUTORIAL, LAB BOOK, ICA-D, ICA, LAB TEST 1, LAB TEST 2, LAB TEST	Unfreeze
7	SLH31	SELF LEARNING COURSE I-HSS-18	ESE, ASSIGNMENT	Unfreeze

Step 5: Marks filling process is performed in defined Tools

Link for Excel sheet:

RWork

svet.rwork.tech/assign_marks/ViewUpdateMarks?year=118&class_name=3&term=1&course_id=1423&tool_id=308&division=83&program_name=3

Tools - Evaluation & Attainment

ESE Marks

Academic Year 2018-19

Degree Level UNDER GRADUATE

Class THIRD YEAR

Division A

Tool Minimum Marks 28

Tool Maximum Marks 70

Target Level (% Target Marks for CO Attainment)

Sc. No. Roll No. Name of Student Obtained Marks

1 131ET11061 BALASABER TAWARE DADASAHER 5

2 141ET11051 PATHAK PRAJAKTA PRAMOD 33

3 141ET11053 KATKAR PUJA APPASAHER 28

4 131ET11004 KALDHONE AMRUTA SANJAY 22

5 131ET11025 KAMBLE SNEHA BHASKAR 12

6 161ET11001 KOLYVENSHALI RAJENDRA 42

7 141ET11002 BHAKADE PRAJAKTA KASHINATH 28

8 161ET11003 BHACHHODE YAMINI VILAS 21

Edit ESE Marks

Academic Year 2018-19

Class THIRD YEAR

Division A

Roll No. of Student 141ET11051

Name of Student PATHAK PRAJAKTA PRAMOD

Round-off Total Marks* YES

Obtained Marks 35

Out of Marks 70

Previous Close Next

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Note: * indicates Mandatory Fields

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Step 6: CO attainment of all Tools (Internal & External)

Link for Excel sheet:

RWork

svet.rwork.tech/marksheet/DirectAttainment/directcoattainment?year=118&class_name=3&term=1&course_id=1423&division=83&programme_name=3&tier=2

Academic Year 2018-19

Degree Level UNDER GRADUATE

Class THIRD YEAR

Division A

Program UNDER GRADUATE IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Department ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Semester SEMESTER I

Course ELECTRO MAGNETIC ENDS & RADIATING SYSTEM (ET311-18)

View*

TIER II

CO attainment CO attainment with Target

Internal Tools

Sl. No.	Tools	ET311-18.1		ET311-18.2		ET311-18.3		ET311-18.4		ET311-18.5		ET311-18.6	
		Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment
1	ISE-1	2	3	2	3	NA	NA	NA	NA	NA	NA	NA	NA
2	OBT-1	NA	NA	2	3	NA	NA	NA	NA	NA	NA	NA	NA
3	THY-1	2	3	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA
4	ISE-2	NA	NA	NA	NA	2	3	2	3	NA	NA	NA	NA
5	OBT-2	NA	NA	NA	NA	2	3	NA	NA	NA	NA	NA	NA
6	THY-2	NA	NA	NA	NA	NA	NA	2	3	NA	NA	NA	NA
7	ISE-3	NA	NA	NA	NA	NA	NA	3	3	1	3	1	3
8	OBT-3	NA	NA	NA	NA	NA	NA	NA	NA	2	3	NA	NA
9	THY-3	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	2	3
10	UT-1	2	3	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA
11	UT-2	NA	NA	NA	NA	2	3	NA	NA	NA	NA	NA	NA
12	UT-3	NA	NA	NA	NA	NA	NA	NA	NA	1	3	NA	NA
13	ASSIGNMENT	3	3	3	3	3	3	3	3	3	3	3	3
14	TUTORIAL	3	3	3	3	3	3	3	3	3	3	3	3
15	PPPE	3	3	3	3	3	3	2	3	3	3	1	3
16	LAB BOOK	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	3	3
17	LAB TEST 1	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	3	3
18	LAB TEST 2	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	3	3
INTERNAL TOOL ATTAINMENT		2.50	3.00	2.40	3.00	2.33	3.00	2.50	3.00	2.00	3.00	2.38	3.00

Internal Tool Weightage (%) 23

External Tools

Step 7: Direct PO attainment of a course

Link for Excel sheet:

Sl. No.	Tool	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment
9	TVET	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	2	3
10	UT-1	2	3	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA
11	UT-2	NA	NA	NA	NA	2	3	NA	NA	NA	NA	NA	NA	NA	NA
12	UT-3	NA	NA	NA	NA	NA	NA	NA	NA	1	3	3	NA	NA	NA
13	ASSIGNMENT	3	3	3	3	3	3	3	3	3	3	3	3	3	3
14	TUTORIAL	3	3	3	3	3	3	3	3	3	3	3	3	3	3
15	PPPT	3	3	2	3	2	3	2	3	2	3	2	3	1	3
16	LAB BOOK	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	3	3
17	LAB TEST 1	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	3	3
18	LAB TEST 2	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	3	3
INTERNAL TOOL ATTAINMENT		2.50	3.00	2.40	3.00	2.33	3.00	2.50	3.00	2.00	3.00	2.38	3.00		

Internal Tool Weightage (%)

External Tools

Sl. No.	Tools	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment
1	EE	2	2	2	2	2	2	2	2	2	2	2	2	2	2
EXTERNAL TOOL ATTAINMENT		2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00

External Tool Weightage (%)

Overall Course CO Attainment

Sl. No.	Tool Type	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment	Target	Attainment
1	Internal	2.50	3.00	2.40	3.00	2.33	3.00	2.50	3.00	2.00	3.00	2.38	3.00	2.35	3.00
2	External	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.00
Overall		2.19	2.20	2.08	2.20	2.07	2.20	2.15	2.20	2.00	2.20	2.08	2.20	2.07	2.20

Remove Weightage Change Tool Weightage Download Report Back

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Step 7: Direct PO attainment of a course

Link for Excel sheet:

Dashboard

Attendance

Employee Record

Student Center

Employee Profile

Academics

Dashboard

Time Table & Attendance

Vision & Mission

Program Management

Course Evaluation Tools

Course Management

Student Enrollment

Evaluation

Tool - CO & BL Linking

Tool - Evaluation & Attainment

Course CO Attainment

Direct PO Attainment

Indirect Attainment

First Year Academics

Examination

Document Issuance

Library Management

HSSA - UG Tier II

HSSA - HSSA/PGDM

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Course CO Attainment

Direct PO Attainment

Indirect Attainment

First Year Academics

Examination

Document Issuance

Library Management

HSSA - UG Tier II

HSSA - HSSA/PGDM

RWork Settings

Direct PO Attainment

Direct PO Attainment

Academic Year 2018-19

Degree Level UNDERGRADUATE

Class T HSSA

Division A

Program UNDERGRADUATE IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Department ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Semester SEMESTER I

Note: * indicates Mandatory Field

Tier*
TIER II

PO Attainment information

Sl. No.	Course Name	Course Code	Internal Attainment	External Attainment	Overall CO Attainment	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PO13	PO14	PO15	PO16
1	ELECTROMAGNETIC WAVES & GUIDING SYSTEM	ET221-18	3.00	2.00	2.20	1.47	1.47	1.47	2.20	1.47	1.47	0.75	0.75	1.47	0.75	1.47	1.47	2.20	2.20	2.20	
2	PRINCIPLES OF DIGITAL COMMUNICATION	ET222-18	3.00	3.00	3.00	3.00	1.50	1.50	1.50	1.50	NA	1.33	NA	1.50	1.50	1.17	3.00	1.17	2.50		
3	SOFTWARE ENGINEERING & PROJECT MANAGEMENT SYSTEM	ET223-18	2.64	3.00	2.80	2.12	1.63	2.28	2.61	2.90	2.76	2.76	1.35	2.28	1.30	2.61	1.30	NA	0.98	2.44	
4	DIGITAL SIGNAL PROCESSING	ET224-18	2.59	3.00	2.82	2.82	2.75	1.95	2.82	2.82	0.97	1.46	0.97	1.78	0.97	NA	1.95	2.92	NA	1.94	
5	MICROCONTROLLER - I (8085)	ET225-18	2.66	3.00	2.83	2.94	NA	2.95	1.95	1.95	1.95	1.95	NA	NA	1.95	2.83	0.98	2.54	2.93	2.25	
6	SELF-LEARNING COURSE - I (HSS-18)	SL431	3.00	3.00	3.00	1.00	1.00	NA	NA	NA	2.30	NA	2.00	NA	NA	NA	NA	NA	1.00	3.00	
7	ELECTRONIC SOFTWARE LAB-II	ET226-18	3.00	NA	3.00	3.00	3.00	2.00	1.00	2.00	1.00	NA	NA	1.00	NA	1.00	2.00	3.00	3.00	3.00	
Direct Attainment						2.26	1.81	2.08	2.03	2.13	1.78	1.69	1.25	1.67	1.24	1.80	1.48	2.73	1.89	2.25	

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Indirect Target & Attainment

Indirect Target & Attainment

Academic Year*
2018-19

Program**
UNDER GRADUATE IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (1ST)

Note: * Indicates Mandatory Fields

Level of Co-relation:
No Co-relation: 0 Low Co-relation: 1 Medium Co-relation: 2 High Co-relation: 3

Indirect Target Indirect Attainment

Sl. No.	Method Name	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PS01	PS02	PS03
1	EMPLOYEE METHOD	3.00	3.00	NA	NA	3.00	NA	3.00	3.00	NA	3.00	3.00	3.00	NA	NA	NA
2	ALUMNI FEEDBACK	3.00	3.00	NA	NA	NA	NA	NA	NA	2.00	2.00	1.00	NA	NA	NA	NA
3	GUEST LECTURE FEEDBACK	2.00	NA	2.00	2.70	2.00	2.00	2.00	2.00	2.00	2.00	2.00	2.70	NA	NA	NA
4	INDUSTRIAL VISIT FEEDBACK	NA	3.00	NA	NA	3.00	3.00	NA	3.00	3.00	NA	1.00	NA	NA	NA	NA
5	COURSE END SURVEY	NA	NA	NA	NA	NA	NA	NA	3.00	3.00	3.00	3.00	3.00	NA	NA	NA
6	EXIT SURVEY	2.00	3.00	2.00	NA	3.00	NA	NA	3.00	2.00	NA	2.00	NA	NA	NA	NA
7	PARENT SURVEY	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA	NA
Total Attainment		2.23	2.50	2.45	2.70	2.48	2.00	2.00	2.82	2.58	2.48	2.38	2.00	NA	NA	NA

Indirect Assessment Weightage (%) 20

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